











# **ACTH SERIES**

#### 125°C WIDE TEMPERATURE TYPE

**ALUMINUM SOLID ELECTROLYTIC CAPACITOR** ▲ STACKED type Very high ripple current up to 7.5A at 100kHz/45°C Ultra-low ESR up to  $6m\Omega$  at  $100kHz/20^{\circ}C$ Low drift and stable electrical characteristics over lifetime No liquid electrolyte ▲ No dry-out effect Moisture Sensitivity Level ▲ MSL 3

Low height with 1.9mm ideal for space critical applications

#### **SPECIFICATION**

Item		Characteristics				
Category Temperature Range		-55°C to +125°C				
Rated Voltage Range	$V_R$	2.5V <sub>DC</sub> to 25V <sub>DC</sub>				
Rated Capacitance Range	$C_R$	15μF to 330μF				
Capacitance Tolerance	ΔC	±20% ▲ +10 to -35%				
Surge Voltage • At 15 to 35°C	Vs	$V_S = 1.25 \times V_R$	$V_R$ : $2V_{DC}$ to $2.5V_{DC}$			
Surge Voltage - At 15 to 55 C	Vs	$V_S = 1.15 \times V_R$	$V_R{:}~16V_{DC}~to~25V_{DC}$			
Dissipation Factor • At 20°C; 120Hz	tan δ	0.1 max.				
		$I_{LEAK} = 0.1 \times C_R \times V_R$	$V_R$ : $2V_{DC}$ to $2.5V_{DC}$			
Leakage Current • At 20°C; after 2min.	I <sub>LEAK</sub>	$I_{LEAK} = 0.3 \times C_R \times V_R$	$V_R$ : 16 $V_{DC}$ to 25 $V_{DC}$			
		With $I_{LEAK}$ ( $\mu A$ ) $\blacktriangle$ $C_R$ ( $\mu F$ ) $\blacktriangle$ $V_R$ ( $V_{DC}$ )				
	Test	125°C ▲ 1000hrs ▲ V <sub>R</sub> applied				
	Appearance	No significant damage				
Endurance	ΔC/C <sub>R</sub>	≤ ±20% of the initial value				
Endurance	tan δ	≤ 200% of the initial specified value				
		≤ 300% of the initial specified value	$V_R$ : $2V_{DC}$ to $2.5V_{DC}$			
	I <sub>LEAK</sub>	≤ The initial specified value	$V_R$ : 16 $V_{DC}$ to 25 $V_{DC}$			
	Test	60°C ▲ 90 to 95% RH ▲ 500hrs • No voltage applied				
	Appearance	No significant damage				
	AC/C	+70% / -20% of the initial value	$V_R$ : $2V_{DC}$ to $2.5V_{DC}$			
Damp Heat (Steady State)	ΔC/C <sub>R</sub>	+60% / -20% of the initial value	$V_R$ : 16 $V_{DC}$ to 25 $V_{DC}$			
	tan δ	≤ 200% of the initial specified value				
		≤ The initial specified value	$V_R$ : $2V_{DC}$ to $2.5V_{DC}$			
	I <sub>LEAK</sub>	≤ 300% of the initial specified value	$V_R$ : 16 $V_{DC}$ to 25 $V_{DC}$			
		1000 cycles and each one includes c	harge with V <sub>s</sub>			
	Test	specified at 15°C to 35°C for 0.5min through a protective				
		resistor (R=1k $\Omega$ ) and discharge for 5.5min.				
Surge Voltage	Appearance	No significant damage				
	ΔC/C <sub>R</sub>	≤ ±10% of the initial value				
	tan δ	≤ The initial specified value				
	I <sub>LEAK</sub>	≤ The initial specified value				



### **ELECTRICAL CHARACTERISTICS**

V <sub>R DC</sub>	$C_R$	Din	ensions (mm)		I <sub>LEAK</sub> 20°C	ESR 20°C	I <sub>R</sub> ≤ 45°C	Part Number Note 1
(V)	(μF)	(μF) L W H 2min (μA)		I W I H I	100kHz (mΩ)	100kHz (mA)	rait Nullibei	
	330	7.3	4.3	1.9	66	9	6300	ACTH2R0S331E09
2	330	7.3	4.3	1.9	66	9	6300	ACTH2R0S331E09Y
	330	7.3	4.3	1.9	66	6	7500	ACTH2R0S331E06
	330	7.3	4.3	1.9	82.5	9	6300	ACTH2R5S331E09
2.5	330	7.3	4.3	1.9	82.5	9	6300	ACTH2R5S331E09Y
	330	7.3	4.3	1.9	82.5	6	7500	ACTH2R5S331E06
16	47	7.3	4.3	1.9	225.6	40	3200	ACTH160S470E40
16	56	7.3	4.3	1.9	268.8	40	3200	ACTH160S560E40
25	15	7.3	4.3	1.9	112.5	40	3200	ACTH250S150E40
25	33	7.3	4.3	1.9	247.5	40	3200	ACTH250S330E40

#### Notes

1 Part number shows the standard Tape/Reel version

#### **TEMPERATURE CORRECTION FACTOR**

Temperature Correction Factor of Permissible Ripple Current								
Rated Voltage V <sub>R</sub>	Surface Temperature	≤ 45°C	45°C < T <sub>S</sub> ≤ 85°C	85°C < T <sub>S</sub> ≤ 105°C	125°C < T <sub>S</sub>			
2V <sub>DC</sub> to 2.5V <sub>DC</sub>	Coefficient	1	0.7	0.25	0.25			
16V <sub>DC</sub> to 25V <sub>DC</sub>	Coefficient	1	0.8	0.5	0.25			

### **APPLICATIONS**

CPU, FPGA and IC Buffering	High Frequency Applications	Substitution of MLCC Banks	USB Power Supplies & Banks	Voltage Stabilizing in LED Panels
	Out		<del></del>	



# REFERENCE DATA Δ ACTH2R5S331E06 Δ 330μF Δ 2.5V Δ 6mΩ

Fig. 1 • Frequency Characteristics of ESR & |Z|

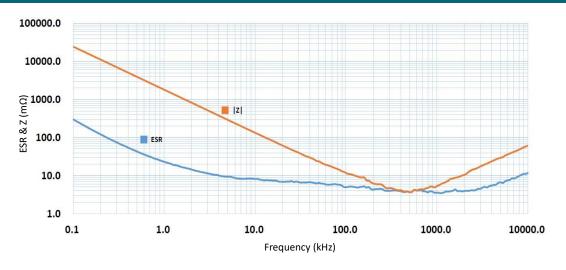


Fig. 2 • Frequency Characteristics of C (μF)

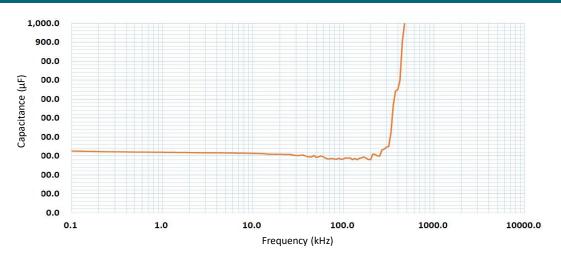
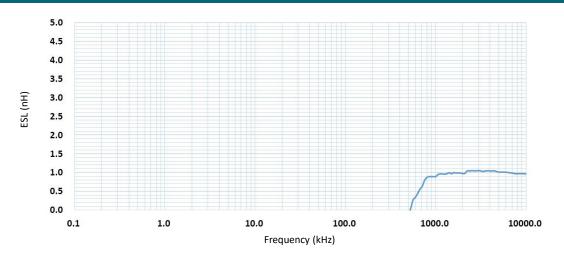


Fig. 3 • Frequency Characteristics of ESL (nH)



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# REFERENCE DATA Δ ACTH160S560E40 Δ 56μF Δ 16V Δ 40mΩ

Fig. 4 • Frequency Characteristics of ESR & |Z|

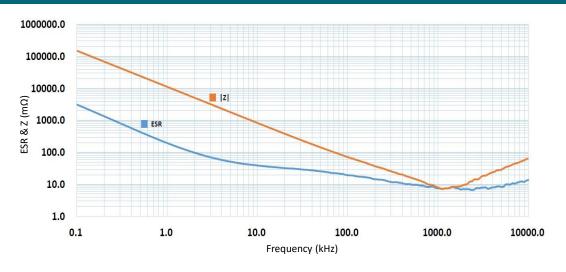


Fig. 5 • Frequency Characteristics of C (μF)

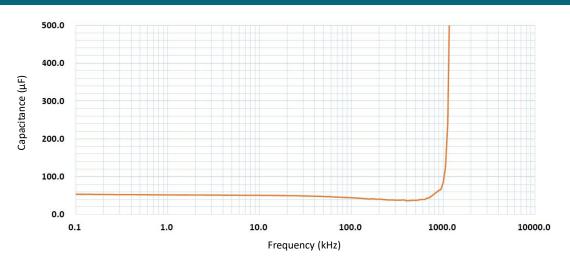
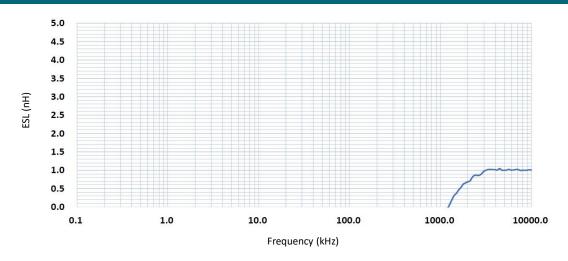


Fig. 6 • Frequency Characteristics of ESL (nH)



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#### PACKAGE OUTLINE ▲ All dimensions in mm

Dimensions								
	Case Size: S	Dimension (mm)	Tolerance (mm)					
	L	7.3	± 0.3					
WA WA	WA	4.3	± 0.3					
H RW	WB	2.4	± 0.2					
	Н	1.9	± 0.2					
<del></del>	Р	1.3	± 0.2					

#### **PRODUCT CODE**

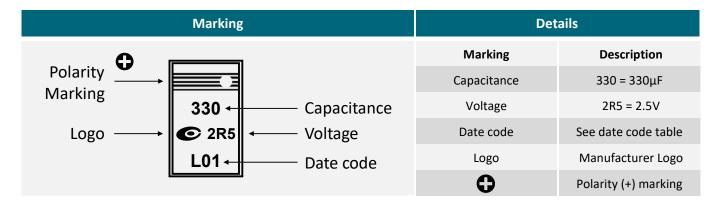
Example: ACTH series  $\blacktriangle$  330 $\mu$ F  $\blacktriangle$  2.5 $V_{DC}$   $\blacktriangle$  +10 to -35%  $\blacktriangle$  9m $\Omega$   $\blacktriangle$  Tape/Reel

AC	тн	21	R5	S		331		E09		Y	
Ser	ies	Rat Volt (V	age	Package Code		Capacitance Code <sup>Note 1</sup> (μF)		ESR		Suffix for Capacitance Tolerance	
Code	Series	Code	VDC	Code	L x W x H mm	Code	μF	Code	mΩ	Code	Tol. in %
ACTH	ACTH	2R0 2R5 160 250	2.0 2.5 16 25	S	7.3x4.3x1.9	150 330 560 331	15 33 56 330	E06 E09 E40	6 9 40	Blank Y	±20 +10 to -35

#### Note:

 $\begin{tabular}{ll} \textbf{Capacitance code expressed in $\mu$F. The first two digits represent significant figures.} \\ \textbf{The last digit specifies the total number of zeros to be added.} \\ \end{tabular}$ 

#### **PRODUCT MARKING**



### **DATE CODE**

Example:

Date code

L01:  $L01 = 1^{st}$  week of 2020

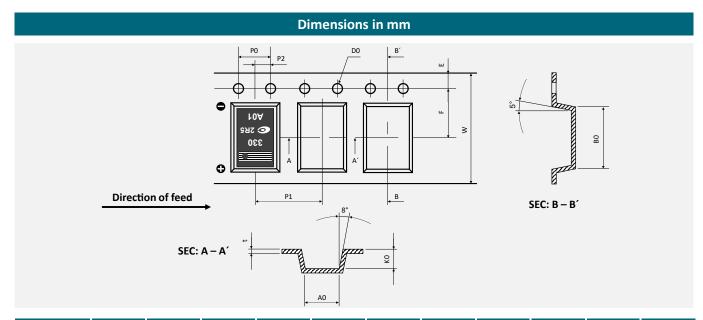
	4	(	01
Ye	ear	W	'eek
L	2020	01	1 <sup>st</sup>
M	2021	02	2 <sup>nd</sup>
V	2030	53	53 <sup>rd</sup>

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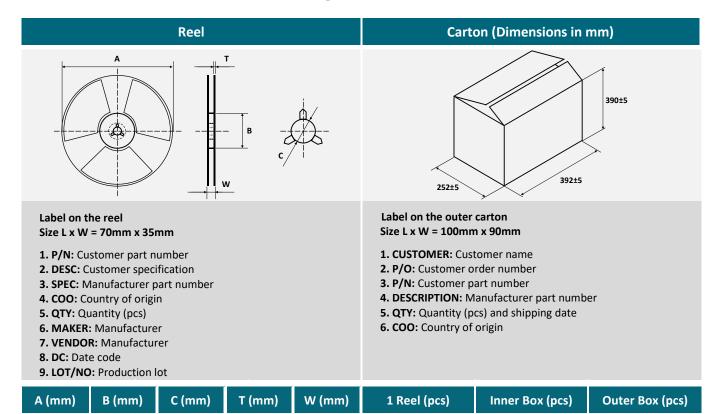


#### **TAPING SPECIFICATION ▲ STACKED TYPE**



	W	P1	E	F	D0	P0	P2	A0	В0	КО	t
Tolerance	± 0.1	± 0.1	± 0.1	± 0.1	+ 0.1 - 0.0	± 0.1	± 0.1	± 0.1	± 0.1	± 0.1	± 0.1
Dimension	12	8	1.75	5.5	1.5	4	2	5	7.6	2.3	0.24

### REEL DIMENSION AND PACKAGING QUANTITY A STACKED TYPE



33600

16800

100 ± 2.0

 $13.2 \pm 0.3$ 

 $2.0 \pm 0.3$ 

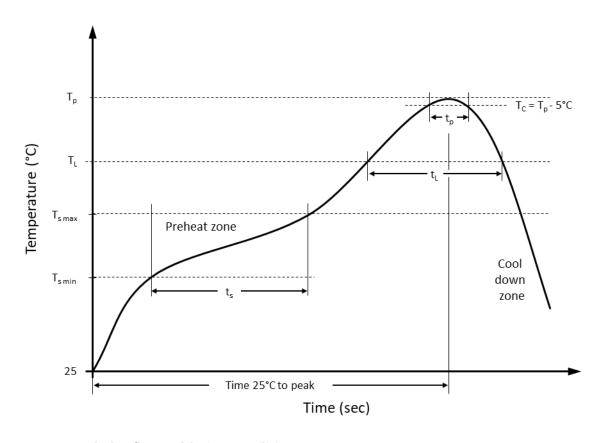
 $330 \pm 1.0$ 

 $13.5 \pm 0.5$ 

2800



### RECOMMENDED REFLOW SOLDERING PROFILE & STACKED PACKAGE



# **Recommended reflow soldering conditions**

Profile Features		Pb-Free Assembly
Preheat temperature min.	T <sub>s min</sub>	150 °C
Preheat temperature max.	$T_{smax}$	200 °C
Preheat time t <sub>s</sub> from T <sub>s min</sub> to T <sub>s max</sub>	t <sub>s</sub>	120 seconds
Ramp-up rate (T <sub>L</sub> to T <sub>P</sub> )		max. 3 °C/second
Liquidous temperature	TL	217 °C
Time t <sub>L</sub> maintained above T <sub>L</sub>	$t_{\scriptscriptstyleL}$	60 to 150 seconds
Peak package body temperature	Tp	See table below
Timeframe of within 5°C below and up to max actual peak body temperature	tp	See table below
Ramp-down rate (T <sub>L</sub> to T <sub>P</sub> )		max. 6 °C/second
Time 25°C to peak temperature		max. 8 minutes

Rated Voltage (V <sub>DC</sub> )	Time > 200°C	Time > 230°C	T <sub>P</sub> Peak Temperature	t <sub>p</sub> Timeframe	Allowed Reflow Runs
24- 25	2 to 25 90 sec. max.	40	260 °C	Max. 5 sec	Max. twice
2 to 25		40 sec. max.	250 °C	Max. 10 sec	Max. three times



#### **REVISION TABLE**

Revision	Date	Status	Notes
001	01/10/2021	Initial release	Initial publication

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