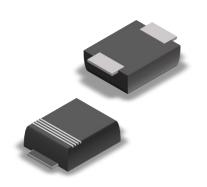


# B2D04065V1

MGT **A** Manufacturer Group of Technology

## 650V 🛦 4A 🛦 SIC SCHOTTKY DIODE

SILICON CARBIDE SiC SCHOTTKY DIODE ▲ SMD type Excellent surge capability Easy paralleling due to positive V<sub>F</sub> temperature coefficient SMBF package ▲ Epoxy meets UL94V-0 ▲ MSL3 Temperature independent switching Ultra-low forward voltage and high surge current





RoHS

REACH

## **SPECIFICATION**

Item (T <sub>c</sub> = 25°C, unless otherwise noted)	Characteristics	
Operating Temperature Range	TJ	-55°C to +175°C
Storage Temperature Range	Ts	-55°C to +175°C
Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	650V
Continuous Forward Current at $T_c = 135^{\circ}C^{\text{Note 1}}$	I <sub>F</sub>	4A
Total Capacitive Charge (TJ = 25°C)	Qc	12nC
Capacitance Stored Energy ( $V_R = 400V$ )	Ec	3µЈ
Diode Forward Voltage (T <sub>J</sub> = 175°C, I <sub>F</sub> = 4A)	V <sub>F</sub>	1.5V
Power Dissipation Note 1	Ρ <sub>τοτ</sub>	25W

Notes:

1. This value is rated according to  $R_{\theta,\text{JC}}$ 

## **APPLICATIONS**

EV Charging	Industrial Inverters	Motors & Drives	Power Factor Correction	Renewable Energy	SMPS	UPS
€Ո≉			PFC	*		

### **PIN DESCRIPTION**

Circuit Diagram	Outline • Top View	Pin No.	Description
		1 2	Cathode Anode

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## **ABSOLUT MAXIMUM RATINGS A** T<sub>c</sub> = 25°C, unless otherwise noted

ltem	Condition	Symbol		Unit
Repetitive Peak Reverse Voltage		V <sub>RRM</sub>	650	V
Non-Repetitive Peak Reverse Voltage		V <sub>RSM</sub>	650	V
Continuous Forward Current	T <sub>C</sub> = 25°C <sup>Note 1</sup>	IF	10	А
Continuous Forward Current	T <sub>PCB</sub> = 25°C Note 2	IF	6	А
Continuous Forward Current	T <sub>C</sub> = 135°C <sup>Note 1</sup>	IF	4	А
Continuous Forward Current	T <sub>PCB</sub> = 100°C <sup>Note 2</sup>	I <sub>F</sub>	4	А
Non-Repetitive Forward Surge Current	$T_{C}$ = 25°C, $t_{p}$ = 10ms, Half Sine Wave	I <sub>FSM</sub>	32	А
I <sup>2</sup> t Value	T <sub>C</sub> = 25°C, t <sub>p</sub> = 10ms	∫i²dt	5.12	A <sup>2</sup> s
Power Dissipation Note 1	T <sub>C</sub> = 25°C	P <sub>TOT_CASE</sub>	25	W
Power Dissipation Note 2	T <sub>C</sub> = 110°C	P <sub>TOT_CASE</sub>	10	W
Power Dissipation Note 1	T <sub>C</sub> = 25°C	P <sub>TOT_PCB</sub>	12	W
Power Dissipation Note 2	T <sub>C</sub> = 110°C	P <sub>TOT_PCB</sub>	5	W
Operating Junction Temperature		TJ	-55 to +175	°C
Storage Temperature Range		T <sub>STG</sub>	-55 to +175	°C

## **ELECTRICAL CHARACTERISTICS**

ltem	Condition	Symbol	Min.	Тур.	Max.	Unit
Static Characteristics						
DC Blocking Voltage	T <sub>J</sub> = 25°C	V <sub>DC</sub>	650			V
Diode Forward Voltage	I <sub>F</sub> = 4A, T <sub>J</sub> = 25°C	VF		1.31	1.60	V
Diode Forward Voltage	I <sub>F</sub> = 4A, T <sub>J</sub> = 175°C	VF		1.50	2.00	V
Reverse Current	V <sub>R</sub> = 650V, T <sub>J</sub> = 25°C	I <sub>R</sub>		1	60	μΑ
Reverse Current	V <sub>R</sub> = 650V, T <sub>J</sub> = 175°C	I <sub>R</sub>		10	100	μΑ
Item	Condition	Symbol	Min.	Тур.	Max.	Unit
Dynamic Characteristics		eyniser		.,		
Total Capacitive Charge	$V_{R} = 400V, T_{J} = 25^{\circ}C$ $Q_{C} = \int_{0}^{V_{R}} C(V) dV$	Q <sub>C</sub>		12		nC
Total Capacitance	$V_R = 1V, f = 1MHz, T_J = 25^{\circ}C$	С		183		рF
Total Capacitance	V <sub>R</sub> = 300V, f = 1MHz, T <sub>J</sub> = 25°C	С		21		рF
Total Capacitance	V <sub>R</sub> = 600V, f = 1MHz, T <sub>J</sub> = 25°C	С		20.5		pF
Capacitance Stored Energy	V <sub>R</sub> = 400V, T <sub>J</sub> = 25°C	Ec		3		μ

Notes:

1. This value is rated according to  $R_{\theta,JC}$ 

2. This value is rated according to  $R_{\theta,PCB}$ 

## THERMAL RESISTANCE PERFORMANCE

Item	Symbol	Min.	Тур.	Max.	Unit
Thermal Resistance, Junction to Case	R <sub>θ,JC</sub>		6		K/W
Thermal Resistance, Junction to PCB Note 3	$R_{\theta,PCB}$		12.13		K/W

Notes:

3. When mounted on a 1-inch² FR-4, 2 Oz copper board, t < 10 s  $\,$ 

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15

10

5

0

40

60

80

100

*T*<sub>c</sub>(°C)

120

## **REFERENCE DATA A TYPICAL PERFORMANCE**

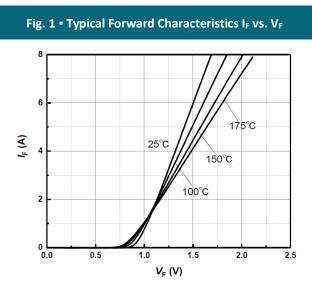


Fig. 2 • Typical Reverse Current I<sub>R</sub> as function of Reverse Voltage V<sub>R</sub>

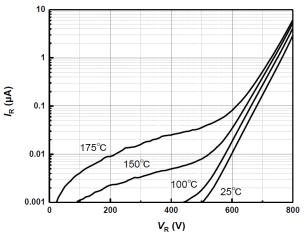
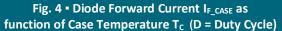


Fig. 3 • Diode Forward Current I<sub>F\_PCB</sub> as function of Case Temperature  $T_c$  (D = Duty Cycle) 30 D=1 D=0.7 25 D=0.5 D=0.3 • • D=0.2 20 •• D=0.1 I<sub>F-PCB</sub> (A)



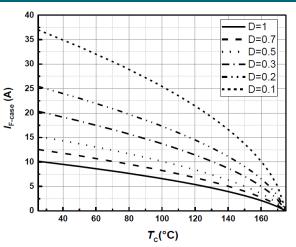


Fig. 5 • Typical Capacitance C as function of Reverse Voltage  $V_R$ , C =f( $V_R$ ), T<sub>J</sub> = 25°C, f = 1MHz

140

160

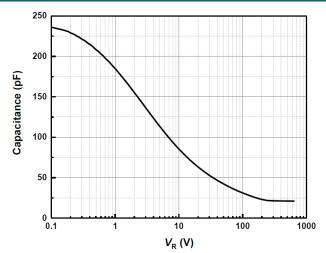
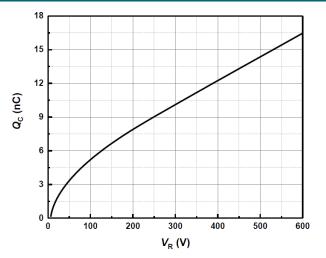


Fig. 6 • Typical Reverse Charge Q<sub>c</sub> as function of Reverse Voltage V<sub>R</sub>



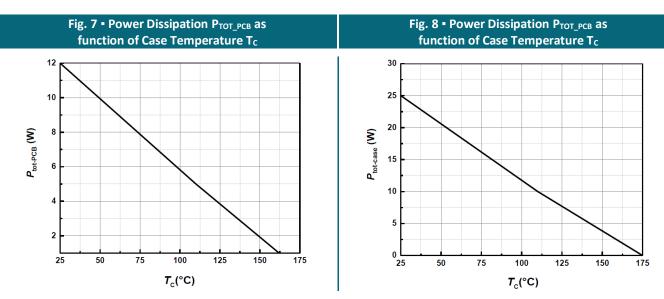
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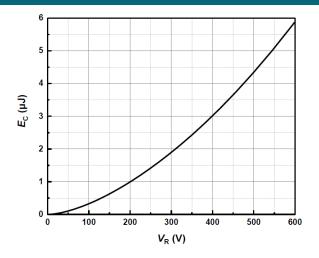
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## **REFERENCE DATA A TYPICAL PERFORMANCE**



#### Fig. 9 - Capacitance Stored Energy





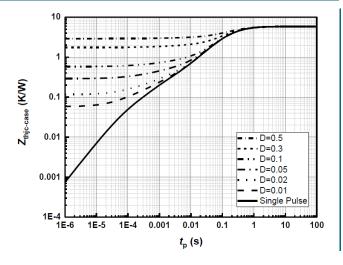
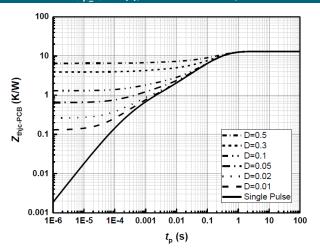


Fig. 10 • Maximum Transient Thermal Impedance, Z<sub>thjc\_PCB</sub> = f(t), Parameter: D = t/T



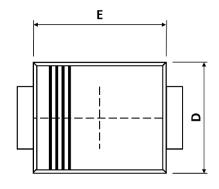
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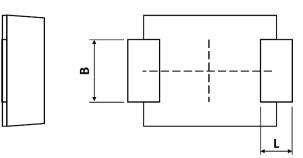
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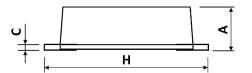




## PACKAGE OUTLINE **A** SMBF PACKAGE





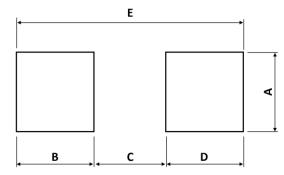


Sym	Millimeters (Min.)	Millimeters (Typ.)	Millimeters (Max.)	Sym	Millimeters (Min.)	Millimeters (Typ.)	Millimeters (Max.)
А	1.30	1.35	1.40	E	4.25	4.30	4.35
В	1.98	2.00	2.02	Н	5.20	5.30	5.40
С	0.12	0.15	0.18	L	0.70	-	1.02
D	3.55	3.60	3.65				

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## **RECOMMENDED PAD LAYOUT**



Sym	Millimeters (Min.)	Millimeters (Typ.)	Millimeters (Max.)
А	2.20	-	-
В	1.90	-	-
С	-	-	2.40
D	1.90	-	-
E		6.20 REF	

Notes:

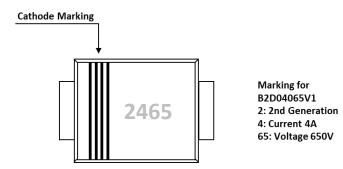
1. The suggested land pattern dimensions have been provided for reference only.

2. For further information, please reference document IPC-7351A.

## **ORDERING INFORMATION**

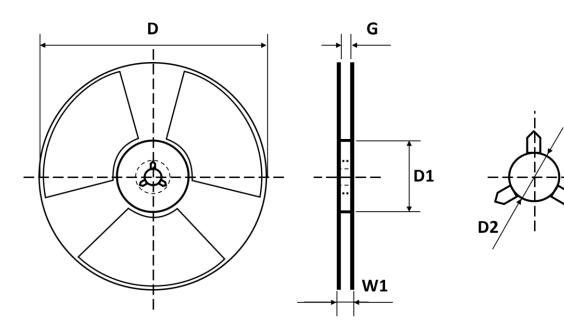
Part Number	Package	Packing	Reel Qty.	Inner Box Qty.	Outer Box Qty.
B2D04065V1	SMBF	Reel	5,000pcs	10,000pcs	50,000pcs

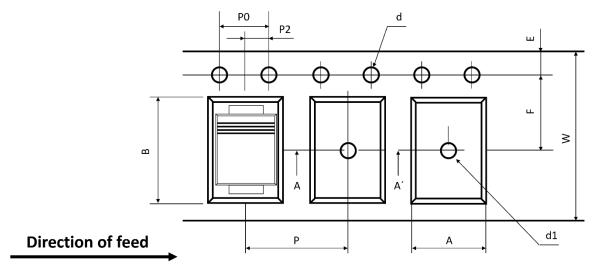
## **PART MARKING**

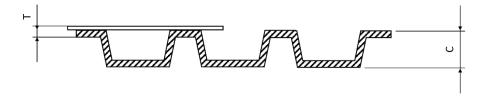




#### **REEL AND TAPE DIMENSIONS** A All dimensions in mm







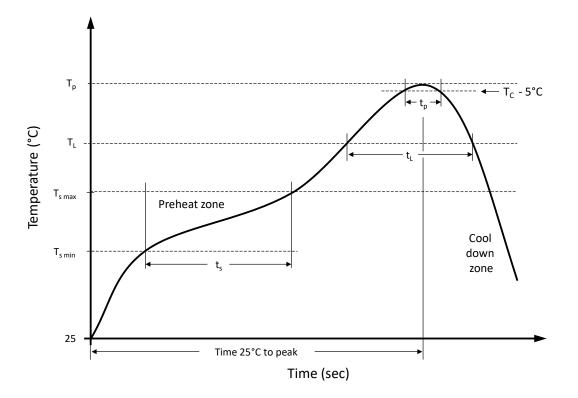
Package	W	Α	В	С	d1	D	Е	F	Р	P0	т	D	D1	D2	G	W1
SMBF	12.00	3.90	5.70	1.40	1.5	1.50	1.75	5.50	8.00	4.00	0.20	330	50	13.00	12.40	18.0
SIVIDE	±0.30	±0.10	±0.10	±0.10	Max.	±0.10	±0.10	±0.10	±0.10	±0.10	±0.10	±0.30	Min.	±0.50	Min.	Min.

Note: All dimensions meet EIA-481-D requirements.

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## **RECOMMENDED REFLOW SOLDERING PROFILE**



#### **Recommended reflow soldering conditions** ▲ **Refer to JEDEC J-STD-020E**

Profile Features		Sn-Pb Eutetic Assembly	Pb-Free Assembly
Preheat temperature min.	$T_{smin}$	100 °C	150 °C
Preheat temperature max.	$T_{s max}$	150 °C	200 °C
Preheat time $t_s$ from $T_{s min}$ to $T_{s max}$	ts	120 seconds	120 seconds
Ramp-up rate (T <sub>L</sub> to T <sub>p</sub> )		max. 3 °C/second	max. 3 °C/second
Liquidous temperature	ΤL	183 °C	217 °C
Time $t_L$ maintained above $T_L$	tL	150 seconds max.	150 seconds max.
Peak package body temperature	Tp	235°C	260°C
Timeframe of within 5°C below and up to max actual peak body temperature	tp	20 seconds max.	30 seconds max.
Ramp-down rate ( $T_L$ to $T_p$ )		max. 6 °C/second	max. 6 °C/second
Time 25°C to peak temperature		max. 6 minutes	max. 8 minutes



## **REVISION TABLE**

Revision	Date	Status	Notes
001	30/09/2022	Initial release	Initial publication

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