









CED05N65

650V ▲ 2Ω ▲ 4A ▲ Si MOSFET

SILICON Si MOSFET ▲ THT type

N-channel enhancement mode

UL94V-0 rated flame retardant epoxy

TO251 (E-PAK) package

Super high dense cell density for extremely low R_{DS(ON)} **High power and current handling capability**

MAXIMUM RATINGS

Parameter (T _C = 25°C, unless otherwise noted)		Characteristics
Drain-Source Voltage	V _{DS}	650V
Gate-Source Voltage	V _{GS}	±30V
Continuous Drain Current at T _C = 25°C	I D	4A
Continuous Drain Current at T _C = 100°C	I D	2.5A
Pulsed Drain Current Note 1	I _{DM}	16A
Maximum Power Dissipation at T _C = 25°C	P _D	56W
Power Dissipation Derating above 25°C	ΔP_D	0.45W/°C
Single Pulsed Avalanche Energy Note 4	E _{AS}	43mJ
Single Pulsed Avalanche Current Note 4	I _{AS}	3.5A
Operating and Storage Temperature Range	T _J , T _{STG}	-55°C to +150°C

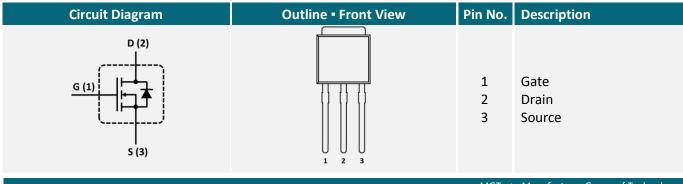
THERMAL CHARACTERISTICS

Parameter	Symbol	Limit
Thermal Resistance, Junction-to-Case	R _{TH_JC}	2.2°C/W
Thermal Resistance, Junction-to-Ambient	R _{TH JA}	50°C/W

APPLICATIONS

EV Charging	Industrial Inverters	Motors & Drives	Power Factor Correction	Renewable Energy	SMPS	UPS
₹		-	PFC	*		

PIN DESCRIPTION





ELECTRICAL CHARACTERISTICS ▲ T_C = 25°C, unless otherwise noted

Item	Condition	Symbol	Min.	Тур.	Max.	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	$V_{GS} = 0V$, $I_D = 250\mu A$	BV_{DSS}	650			V
Zero Gate Voltage Drain Current	$V_{DS} = 650V, V_{GS} = 0V$	I _{DSS}			25	μΑ
Gate Body Leakage Current, Forward	$V_{GS} = 30V, V_{DS} = 0V$	I _{GSSF}			100	nA
Gate Body Leakage Current, Reverse	$V_{GS} = -30V, V_{DS} = 0V$	I_{GSSR}			-100	nA
On Characteristics Note 2						
Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 250 \mu A$	$V_{GS(th)}$	2		4	V
Static Drain-Source On-Resistance	$V_{GS} = 10V, I_D = 2A$	R _{DS(ON)}		2	2.4	Ω
Dynamic Characteristics Note 3						
Input Capacitance	$V_{DS} = 25V$, $V_{GS} = 0V$, $f = 1MHz$	C _{ISS}		570		pF
Output Capacitance	$V_{DS} = 25V$, $V_{GS} = 0V$, $f = 1MHz$	Coss		105		pF
Reverse Transfer Capacitance	$V_{DS} = 25V$, $V_{GS} = 0V$, $f = 1MHz$	C_{RSS}		20		pF
Switching Characteristics Note 3						
Turn-On Delay Time	$V_{DD} = 300V$, $V_{GS} = 10V$, $I_D = 4A$, $R_{G(ext)} = 25\Omega$	$t_{D(ON)}$		23	46	ns
Turn-On Rise Time	V_{DD} = 300V, V_{GS} = 10V, I_D = 4A, $R_{G(ext)}$ = 25 Ω	t _R		13	26	ns
Turn-Off Delay Time	V_{DD} = 300V, V_{GS} = 10V, I_D = 4A, $R_{G(ext)}$ = 25 Ω	t _{D(OFF)}		35	70	ns
Turn-Off Fall Time	V_{DD} = 300V, V_{GS} = 10V, I_D = 4A, $R_{G(ext)}$ = 25 Ω	t _F		11	22	ns
Total Gate Charge	$V_{DD} = 480V$, $V_{GS} = 10V$, $I_D = 4A$	Q_{G}		8.3	11	nC
Gate Source Charge	$V_{DD} = 480V$, $V_{GS} = 10V$, $I_D = 4A$	Q_{GS}		3		nC
Gate Drain Charge	$V_{DD} = 480V$, $V_{GS} = 10V$, $I_D = 4A$	Q_{GD}		3.1		nC
Drain-Source Diode Characteristics and Maximum Ratings						
Drain-Source Diode Forward Current		Is			4	Α
Drain-Source Diode Forward Voltage Note 2	$V_{GS} = 0V$, $I_S = 4A$	V_{SD}			1.5	V

Notes

- 1: Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2: Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 3: Guaranteed by design, not subject to production testing.
- 4: L = 7mH, I_{AS} = 3.5A, V_{DD} = 50V, R_{G} = 25Ω, Starting T_{J} = 25°C



REFERENCE DATA A TYPICAL DEVICE PERFORMANCE



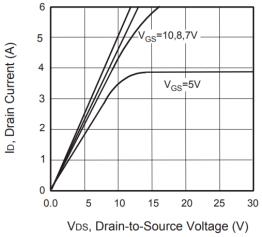


Fig. 2 • Transfer Characteristics

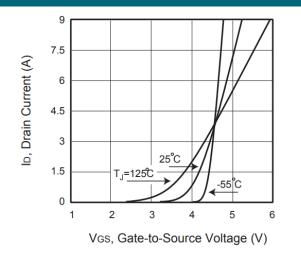


Fig. 3 • Capacitance

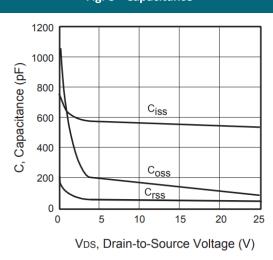


Fig. 4 • On-Resistance Variation with Temperature

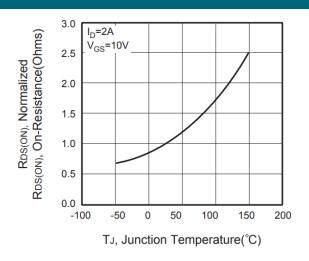


Fig. 5 • Gate Threshold Variation with Temperature

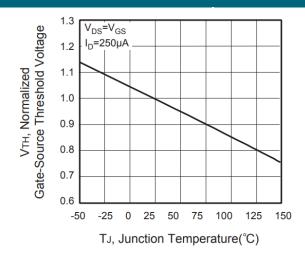
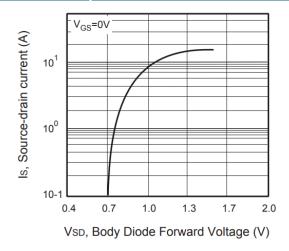


Fig. 6 • Body Diode Forward Voltage Variation with Source Current



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REFERENCE DATA A TYPICAL DEVICE PERFORMANCE

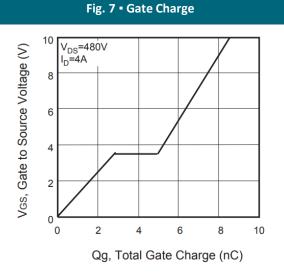


Fig. 8 • Maximum Safe Operating Area

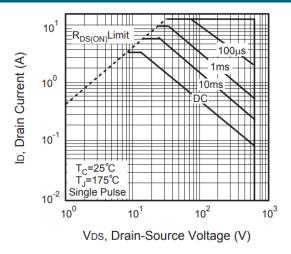
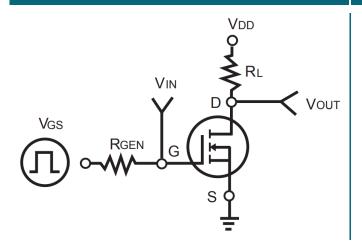


Fig. 9 • Switching Test Circuit

Fig. 10 • Switching Waveforms



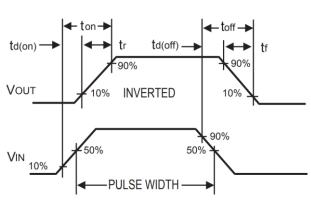
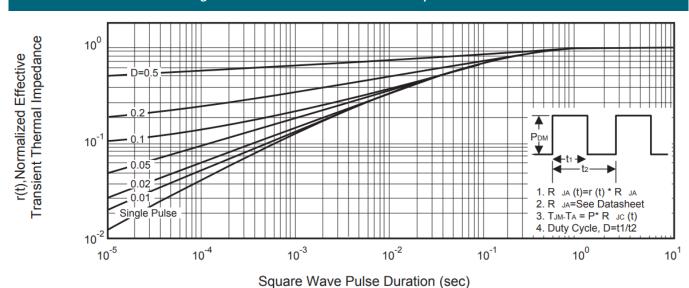


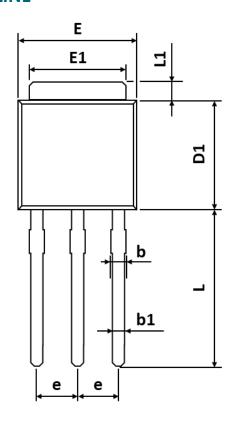
Fig. 11 - Normalized Thermal Transient Impedance Curve

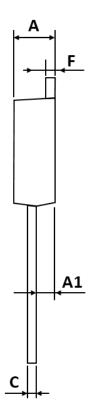


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PACKAGE OUTLINE





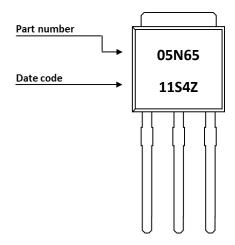
Sym	Millimeters (Min.)	Millimeters (Typ.)	Millimeters (Max.)
А	2.180	-	2.400
A1	0.860	-	1.500
b	0.700	-	0.960
b1	0.700	-	0.860
С	0.400	-	0.610
D1	5.400	-	6.630
Е	6.050	-	7.010
E1	4.950	-	5.460
е	1.980	-	2.590
F	0.400	-	0.890
L	8.500	-	9.650
L1	0.500	-	1.800

ORDERING INFORMATION

Part Number	Package	Packing	Tube Qty.	Inner Box Qty.	Outer Box Qty.
CED05N65	TO251 (E-PAK)	Tube	80pcs	4,000pcs	16,000pcs

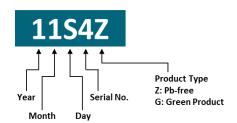


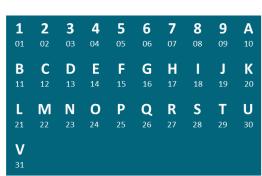
PART MARKING



DATE CODE

Example: 11S4Z



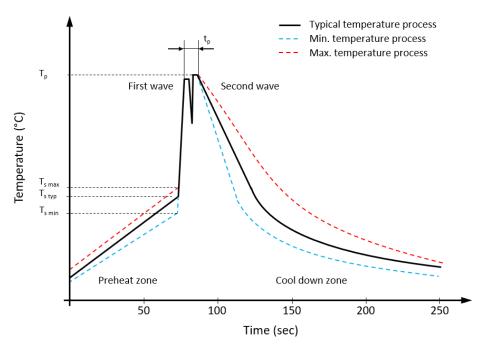


Coding list for "Day"





RECOMMENDED WAVE SOLDERING PROFILE A THT PACKAGE



Classification wave soldering profile ▲ Refer to EN 61760-1: 2006

Profile Features		Value ▲ Sn-Pb Assembly	Value ▲ Pb-free Assembly
Preheat temperature min.	$T_{s min}$	100 °C	100 °C
Preheat temperature typical	T _{s typ}	120 °C	120 °C
Preheat temperature max.	T _{s max}	130 °C	130 °C
Preheat time t_s from T_{smin} to T_{smax}	ts	70 seconds	70 seconds
Peak temperature	Tp	235 °C to 260 °C	245 °C to 260 °C
Time of actual peak temperature	t _p	Max. 10 seconds Max. 5 second each wave	Max. 10 seconds Max. 5 second each wave
Ramp-down date min.		~ 2 °C/second	~ 2 °C/second
Ramp-down rate typical		~ 3.5 °C/second	~ 3.5 °C/second
Ramp-down rate max.		~ 5 °C/second	~ 5 °C/second
Time 25°C to 25°C		4 minutes	4 minutes



REVISION TABLE

Revision	Date	Status	Notes
001	30/09/2022	Initial release	Initial publication

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