









CED1188SA

800V Δ 0.62Ω Δ 6.8A Δ Si MOSFET

SILICON Si MOSFET ▲ THT type

N-channel enhancement mode

UL94V-0 rated flame retardant epoxy

TO251 (E-PAK) package

Super high dense cell density for extremely low R_{DS(ON)} **High power and current handling capability**

MAXIMUM RATINGS

Parameter (T _c = 25°C, unless otherwise noted)		Characteristics
Drain-Source Voltage	V _{DS}	800V
Gate-Source Voltage	V _{GS}	±30V
Continuous Drain Current at T _C = 25°C	I _D	6.8A
Pulsed Drain Current Note 1	I _{DM}	27.2A
Maximum Power Dissipation at T _C = 25°C	P _D	89W
Power Dissipation Derating above 25°C	ΔP _D	0.71W/°C
Single Pulsed Avalanche Energy Note 4	E _{AS}	172.8mJ
Single Pulsed Avalanche Current Note 4	I _{AS}	2.4A
Operating and Storage Temperature Range	T _J , T _{STG}	-55°C to +150°C

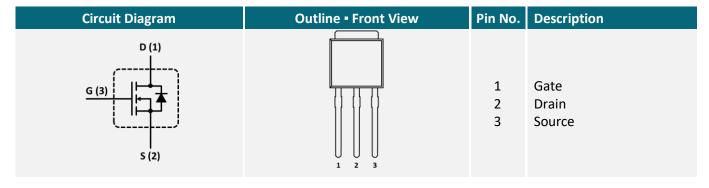
THERMAL CHARACTERISTICS

Parameter	Symbol	Limit
Thermal Resistance, Junction-to-Case	R _{TH_JC}	1.4°C/W
Thermal Resistance, Junction-to-Ambient	R _{TH_JA}	50°C/W

APPLICATIONS

Base Station Power	Battery Chargers	LED Lighting	Power Adapters	SMPS
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PIN DESCRIPTION





ELECTRICAL CHARACTERISTICS ▲ T_C = 25°C, unless otherwise noted

ltem	Condition	Symbol	Min.	Тур.	Max.	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	$V_{GS} = 0V$, $I_D = 250\mu A$	BV_{DSS}	800			V
Zero Gate Voltage Drain Current	$V_{DS} = 800V, V_{GS} = 0V$	I _{DSS}			1	μΑ
Gate Body Leakage Current, Forward	$V_{GS} = 30V$, $V_{DS} = 0V$	I _{GSSF}			100	nA
Gate Body Leakage Current, Reverse	$V_{GS} = -30V, V_{DS} = 0V$	I_{GSSR}			-100	nA
On Characteristics Note 2						
Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_{D} = 250 \mu A$	$V_{GS(th)}$	2		4	V
Static Drain-Source On-Resistance	$V_{GS} = 10V$, $I_D = 4A$	R _{DS(ON)}		0.62	0.72	Ω
Gate Input Resistance	f = 1MHz, Open Drain	R_{G}		6.4		Ω
Dynamic Characteristics Note 3						
Input Capacitance	$V_{DS} = 100V, V_{GS} = 0V, f = 1MHz$	C _{ISS}		685		pF
Output Capacitance	$V_{DS} = 100V$, $V_{GS} = 0V$, $f = 1MHz$	Coss		55		pF
Reverse Transfer Capacitance	$V_{DS} = 100V$, $V_{GS} = 0V$, $f = 1MHz$	C _{RSS}		15		pF
Switching Characteristics Note 3						
Turn-On Delay Time	V_{DD} = 400V, V_{GS} = 10V, I_D = 4A, $R_{G(ext)}$ = 10 Ω	t _{D(ON)}		25		ns
Turn-On Rise Time	V_{DD} = 400V, V_{GS} = 10V, I_D = 4A, $R_{G(ext)}$ = 10 Ω	t_R		9		ns
Turn-Off Delay Time	V_{DD} = 400V, V_{GS} = 10V, I_D = 4A, $R_{G(ext)}$ = 10 Ω	t _{D(OFF)}		45		ns
Turn-Off Fall Time	V_{DD} = 400V, V_{GS} = 10V, I_D = 4A, $R_{G(ext)}$ = 10 Ω	t _F		10		ns
Total Gate Charge	$V_{DD} = 640V$, $V_{GS} = 10V$, $I_D = 4A$	Q_{G}		17		nC
Gate Source Charge	$V_{DD} = 640V$, $V_{GS} = 10V$, $I_D = 4A$	Q_{GS}		3		nC
Gate Drain Charge	$V_{DD} = 640V$, $V_{GS} = 10V$, $I_D = 4A$	Q_{GD}		6		nC
Drain-Source Diode Characteristics a	nd Maximum Ratings					
Drain-Source Diode		Is			6.8	Α
Forward Current		15			0.0	7
Drain-Source Diode Forward Voltage Note 2	$V_{GS} = 0V$, $I_S = 4A$	V_{SD}			1.5	V
Reverse Recovery Time	$I_F = 4A$, di/dt = 100A/ μ s	t_{RR}		341.4		ns
Reverse Recovery Charge	$I_F = 4A$, di/dt = 100A/ μ s	Q_{RR}		1.89		μC
Peak Reverse Recovery Current	$I_F = 4A$, $di/dt = 100A/\mu s$	I_{RR}		10.67		Α

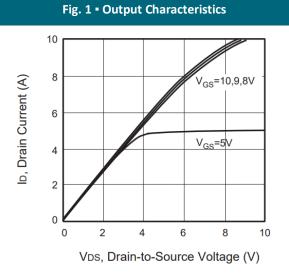
Notes

- 1: Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2: Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 3: Guaranteed by design, not subject to production testing.
- 4: L = 60mH, I_{AS} = 2.4A, V_{DD} = 50V, R_G = 25Ω, Starting T_J = 25°C

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12 10 8 6

b, Drain Current (A)

Fig. 2 • Transfer Characteristics

Vgs, Gate-to-Source Voltage (V)

Fig. 3 • Capacitance

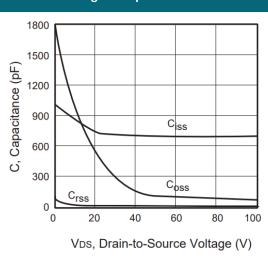


Fig. 4 • On-Resistance Variation with Temperature

 $T_J=125^{\circ}C$

2

0

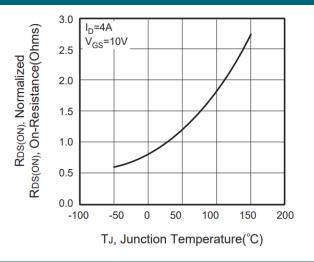


Fig. 5 • Gate Threshold Variation with Temperature

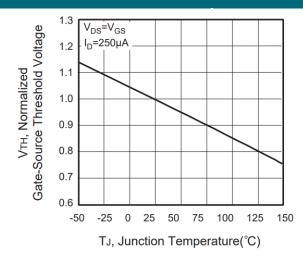
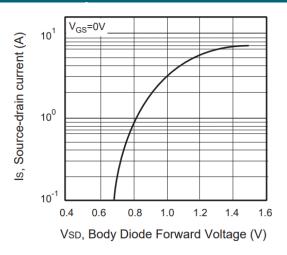


Fig. 6 • Body Diode Forward Voltage Variation with Source Current



MGT

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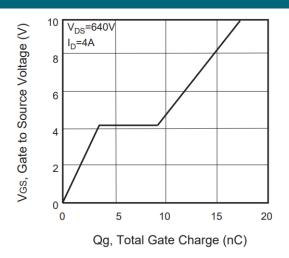


Fig. 8 • Maximum Safe Operating Area

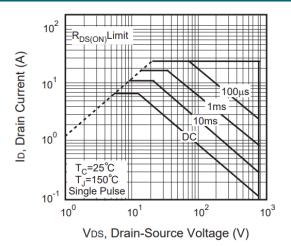
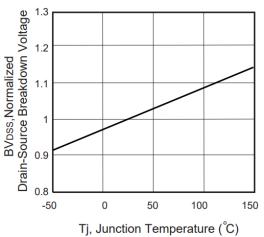


Fig. 9 - Breakdown Voltage Variation vs. Temperature





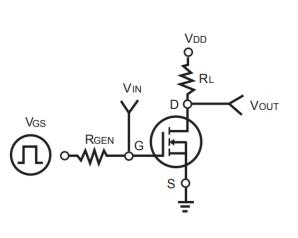
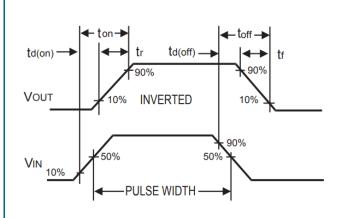


Fig. 11 • Switching Waveforms

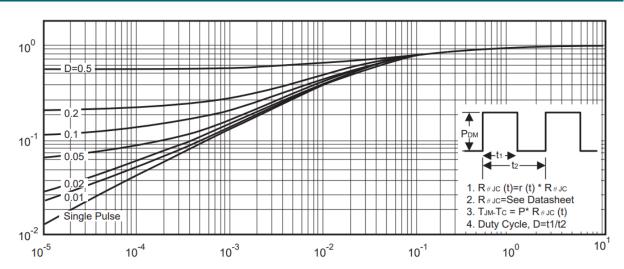




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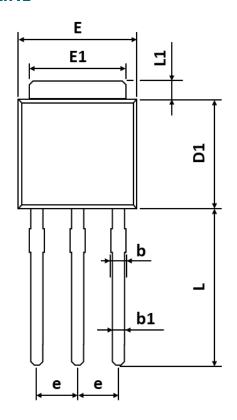
Fig. 12 • Normalized Thermal Transient Impedance Curve

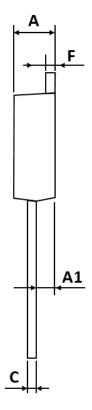






PACKAGE OUTLINE





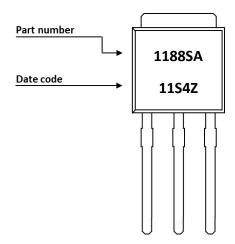
Sym	Millimeters (Min.)	Millimeters (Typ.)	Millimeters (Max.)
Α	2.180	-	2.400
A1	0.860	-	1.500
b	0.700	-	0.960
b1	0.700	-	0.860
С	0.400	-	0.610
D1	5.400	-	6.630
Е	6.050	-	7.010
E1	4.950	-	5.460
е	1.980	-	2.590
F	0.400	-	0.890
L	8.500	-	9.650
L1	0.500	-	1.800

ORDERING INFORMATION

Part Number	Package	Packing	Tube Qty.	Inner Box Qty.	Outer Box Qty.
CED1188SA	TO251 (E-PAK)	Tube	80pcs	4,000pcs	16,000pcs

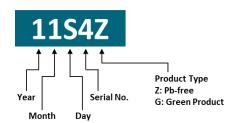


PART MARKING



DATE CODE

Example: 11S4Z



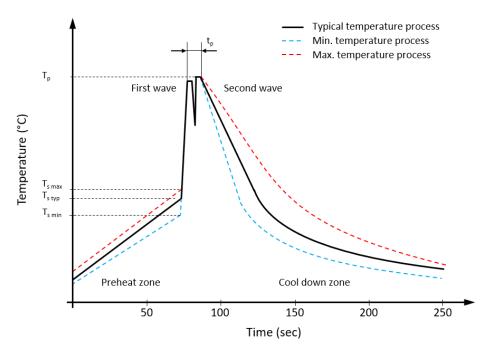


Coding list for "Day"





RECOMMENDED WAVE SOLDERING PROFILE A THT PACKAGE



Classification wave soldering profile ▲ Refer to EN 61760-1: 2006

Profile Features		Value ▲ Sn-Pb Assembly	Value ▲ Pb-free Assembly
Preheat temperature min.	T_{smin}	100 °C	100 °C
Preheat temperature typical	T _{s typ}	120 °C	120 °C
Preheat temperature max.	T_{smax}	130 °C	130 °C
Preheat time t_s from T_{smin} to T_{smax}	t_s	70 seconds	70 seconds
Peak temperature	T_p	235 °C to 260 °C	245 °C to 260 °C
Time of actual peak temperature	t _p	Max. 10 seconds Max. 5 second each wave	Max. 10 seconds Max. 5 second each wave
Ramp-down date min.		~ 2 °C/second	~ 2 °C/second
Ramp-down rate typical		~ 3.5 °C/second	~ 3.5 °C/second
Ramp-down rate max.		~ 5 °C/second	~ 5 °C/second
Time 25°C to 25°C		4 minutes	4 minutes



REVISION TABLE

Revision	Date	Status	Notes
001	30/09/2022	Initial release	Initial publication

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