SILICON (Si) POWER MOSFET ▲ CED14N10



CET MOS

CED14N10

100V ▲ 75mΩ ▲ 12A ▲ Si MOSFET

SILICON Si MOSFET ▲ THT type N-channel enhancement mode UL94V-0 rated flame retardant epoxy TO251 (E-PAK) package Super high dense cell density for extremely low R_{DS(ON)} High power and current handling capability

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FREE

RoHS



Parameter ($T_c = 25^{\circ}C$, unless otherwise noted)		Characteristics
Drain-Source Voltage	V _{DS}	100V
Gate-Source Voltage	V _{GS}	±30V
Continuous Drain Current at T _c = 25°C	I _D	12A
Pulsed Drain Current Note 1	I _{DM} Note4	48A
Maximum Power Dissipation at T _c = 25°C	PD	31W
Power Dissipation Derating above 25°C	ΔP _D	0.25W/°C
Single Pulsed Avalanche Energy Note 4	E _{AS}	9.68mJ
Single Pulsed Avalanche Current Note 4	I _{AS}	4.4A
Operating and Storage Temperature Range	T _J , T _{STG}	-55°C to +150°C

THERMAL CHARACTERISTICS

Parameter	Symbol	Limit
Thermal Resistance, Junction-to-Case	R _{TH_JC}	4°C/W
Thermal Resistance, Junction-to-Ambient Note 2	R _{TH_JA}	50°C/W

APPLICATIONS

Battery Management Systems	E-Bike	Industrial Control	Power Inverter	UPS
+ 4 -	50			

PIN DESCRIPTION

Circuit Diagram	Outline - Front View	Pin No.	Description
D (2)		1	Gate
G (1)		2	Drain
S (3)		3	Source

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ELECTRICAL CHARACTERISTICS A T_c = 25°C, unless otherwise noted

ltem	Condition	Symbol	Min.	Тур.	Max.	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	BV _{DSS}	100			V
Zero Gate Voltage Drain Current	V_{DS} = 100V, V_{GS} = 0V	I _{DSS}			1	μΑ
Gate Body Leakage Current, Forward	$V_{GS} = 30V, V_{DS} = 0V$	I _{GSSF}			100	nA
Gate Body Leakage Current, Reverse	V_{GS} = -30V, V_{DS} = 0V	I _{GSSR}			-100	nA
On Characteristics Note 3						
Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 250 \mu A$	$V_{GS(th)}$	2		4	V
Static Drain-Source On-Resistance	$V_{GS} = 10V, I_{D} = 5A$	R _{DS(ON)}		75	100	mΩ
Static Drain-Source On-Resistance	V _{GS} = 6V, I _D = 5A	R _{DS(ON)}		100	140	mΩ
Dynamic Characteristics Note 3						
Input Capacitance	V_{DS} = 25V, V_{GS} = 0V, f = 1MHz	C _{ISS}		730		рF
Output Capacitance	$V_{DS} = 25V, V_{GS} = 0V, f = 1MHz$	Coss		85		рF
Reverse Transfer Capacitance	V_{DS} = 25V, V_{GS} = 0V, f = 1MHz	C _{RSS}		45		pF
Switching Characteristics Note 3						
Turn-On Delay Time	V_{DD} = 80V, V_{GS} = 10V, I_D = 6A, $R_{G(ext)}$ = 6 Ω	t _{D(ON)}		15		ns
Turn-On Rise Time	V_{DD} = 80V, V_{GS} = 10V, I_{D} = 6A, $R_{\text{G}(\text{ext})}$ = 6 Ω	t _R		6		ns
Turn-Off Delay Time	V_{DD} = 80V, V_{GS} = 10V, I_{D} = 6A, $R_{\text{G(ext)}}$ = 6 Ω	t _{D(OFF)}		25		ns
Turn-Off Fall Time	V_{DD} = 80V, V_{GS} = 10V, I_{D} = 6A, $R_{\text{G(ext)}}$ = 6 Ω	t _F		5		ns
Total Gate Charge	$V_{DS} = 80V, V_{GS} = 10V, I_D = 6A$	Q_{G}		15		nC
Gate Source Charge	$V_{DS} = 80V, V_{GS} = 10V, I_D = 6A$	Q _{GS}		2.8		nC
Gate Drain Charge	V_{DS} = 80V, V_{GS} = 10V, I_{D} = 6A	\mathbf{Q}_{GD}		6.6		nC
Drain-Source Diode Characteristics a	nd Maximum Ratings					
Drain-Source Diode Forward Current ^{Note 2}		١ _s			12	А
Drain-Source Diode Forward Voltage Note 2	$V_{GS} = 0V$, $I_S = 5A$	V_{SD}			1.2	V

Notes

1: Repetitive Rating: Pulse width limited by maximum junction temperature

2: Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

3: Guaranteed by design, not subject to production testing.

4: Pulse width limited by safe operating area.

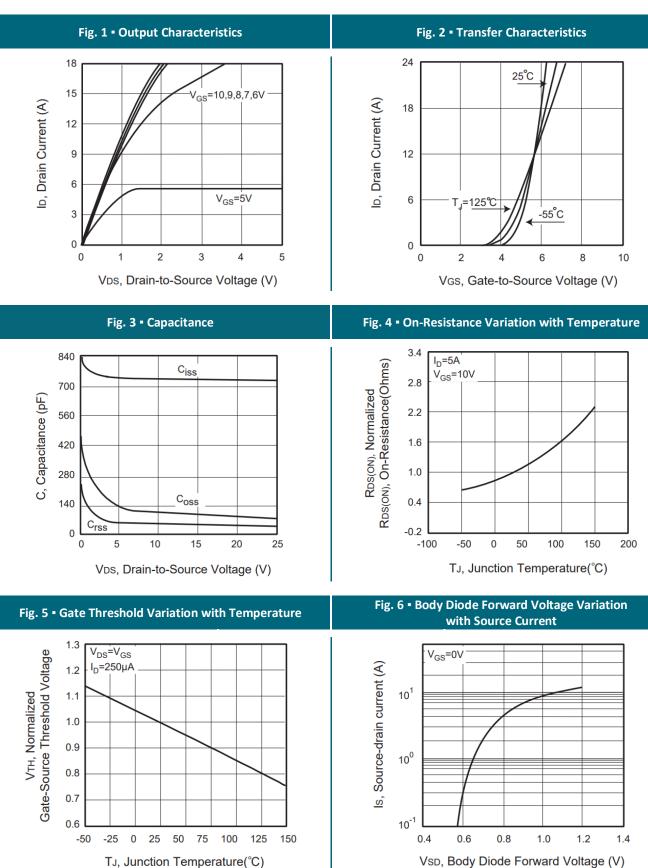
5: L = 1mH, I_{AS} = 4.4A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25°C



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REFERENCE DATA ▲ TYPICAL DEVICE PERFORMANCE



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REFERENCE DATA A TYPICAL DEVICE PERFORMANCE

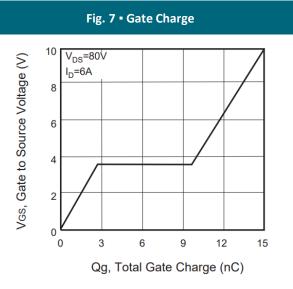
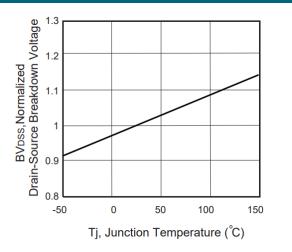


Fig. 9 - Breakdown Voltage Variation vs. Temperature



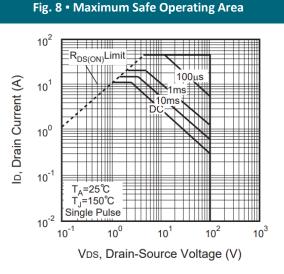


Fig. 11 - Switching Waveforms

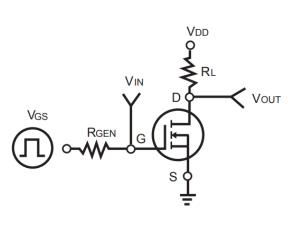
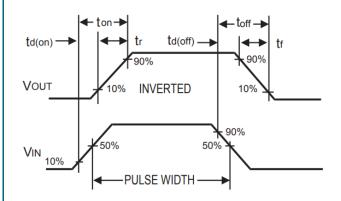


Fig. 10 • Switching Test Circuit



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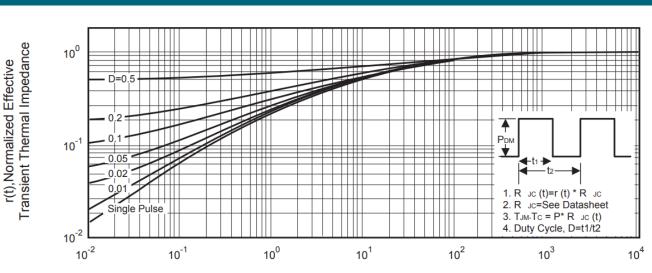


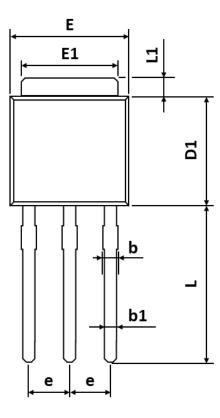
Fig. 12 • Normalized Thermal Transient Impedance Curve

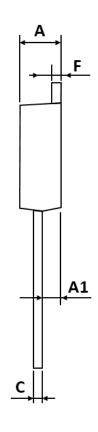
Square Wave Pulse Duration (msec)

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PACKAGE OUTLINE





Sym	Millimeters (Min.)	Millimeters (Typ.)	Millimeters (Max.)
А	2.180	-	2.400
A1	0.860	-	1.500
b	0.700	-	0.960
b1	0.700	-	0.860
С	0.400	-	0.610
D1	5.400	-	6.630
E	6.050	-	7.010
E1	4.950	-	5.460
е	1.980	-	2.590
F	0.400	-	0.890
L	8.500	-	9.650
L1	0.500	-	1.800

ORDERING INFORMATION

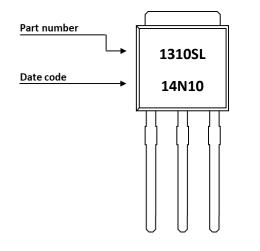
Part Number	Package	Packing	Tube Qty.	Inner Box Qty.	Outer Box Qty.
CED14N10	TO251 (E-PAK)	Tube	80pcs	4,000pcs	16,000pcs

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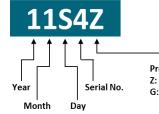
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PART MARKING



DATE CODE

Example: 11S4Z



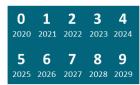
| Product Type Z: Pb-free G: Green Product

	Coding list for "Day"								
1	2	3	4	5	6	7	8	9	A
01	02	03	04	05	06	07	08	09	10
B	C	D	E	F	G	H	 	J	K
11	12	13	14	15	16	17	18	19	20
L	M	N	0	P	Q	R	S	T	U
21	22	23	24	25	26	27	28	29	30
V 31									

Coding list for "Month"

1 Jan	2 Feb		5 May	
7	8	A	B	C
Jul	Aug	Oct	Nov	Dec

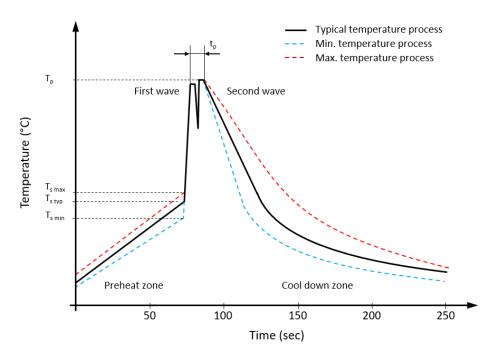
Coding list for "Year"







RECOMMENDED WAVE SOLDERING PROFILE ▲ THT PACKAGE



Classification wave soldering profile ▲ Refer to EN 61760-1: 2006

Profile Features		Value 🛦 Sn-Pb Assembly	Value 🔺 Pb-free Assembly
Preheat temperature min.	$T_{s min}$	100 °C	100 °C
Preheat temperature typical	T _{s typ}	120 °C	120 °C
Preheat temperature max.	$T_{s max}$	130 °C	130 °C
Preheat time t_s from $T_{s min}$ to $T_{s max}$	ts	70 seconds	70 seconds
Peak temperature	Tp	235 °C to 260 °C	245 °C to 260 °C
Time of actual peak temperature	t _p	Max. 10 seconds Max. 5 second each wave	Max. 10 seconds Max. 5 second each wave
Ramp-down date min.		~ 2 °C/second	~ 2 °C/second
Ramp-down rate typical		~ 3.5 °C/second	~ 3.5 °C/second
Ramp-down rate max.		~ 5 °C/second	~ 5 °C/second
Time 25°C to 25°C		4 minutes	4 minutes



REVISION TABLE

Revision	Date	Status	Notes
001	30/09/2022	Initial release	Initial publication

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