









# CED16N10SL

#### 100V ▲ 98mΩ ▲ 12A ▲ Si MOSFET

SILICON Si MOSFET ▲ THT type

N-channel enhancement mode

UL94V-0 rated flame retardant epoxy

TO251 (E-PAK) package

Super high dense cell density for extremely low R<sub>DS(ON)</sub>

High power and current handling capability

#### **MAXIMUM RATINGS**

Parameter ( $T_c$ = 25°C, unless otherwise noted)		Characteristics
Drain-Source Voltage	V <sub>DS</sub>	100V
Gate-Source Voltage	V <sub>GS</sub>	±16V
Continuous Drain Current at T <sub>C</sub> = 25°C	<b>I</b> D	12A
Continuous Drain Current at T <sub>C</sub> = 100°C	<b>I</b> D	8A
Pulsed Drain Current Note 1	I <sub>DM</sub> Note4	48A
Maximum Power Dissipation at T <sub>C</sub> = 25°C	P <sub>D</sub>	36W
Power Dissipation Derating above 25°C	$\Delta P_D$	0.28W/°C
Single Pulsed Avalanche Energy Note 4	E <sub>AS</sub>	36mJ
Single Pulsed Avalanche Current Note 4	I <sub>AS</sub>	12A
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55°C to +150°C

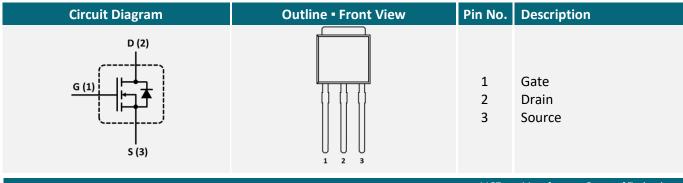
#### THERMAL CHARACTERISTICS

Parameter	Symbol	Limit
Thermal Resistance, Junction-to-Case	R <sub>TH_JC</sub>	3.5°C/W
Thermal Resistance, Junction-to-Ambient Note 2	R <sub>TH JA</sub>	50°C/W

# **APPLICATIONS**

Battery Management Systems	E-Bike	Industrial Control	Power Inverter	UPS
+4-	50			

## **PIN DESCRIPTION**





# **ELECTRICAL CHARACTERISTICS** ▲ T<sub>C</sub> = 25°C, unless otherwise noted

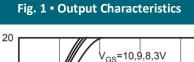
Item	Condition	Symbol	Min.	Тур.	Max.	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	$V_{GS} = 0V$ , $I_D = 250\mu A$	$BV_{DSS}$	100			V
Zero Gate Voltage Drain Current	$V_{DS} = 100V, V_{GS} = 0V$	I <sub>DSS</sub>			1	μΑ
Gate Body Leakage Current, Forward	$V_{GS} = 16V$ , $V_{DS} = 0V$	$I_{GSSF}$			100	nA
Gate Body Leakage Current, Reverse	$V_{GS} = -16V, V_{DS} = 0V$	$I_{GSSR}$			-100	nA
On Characteristics Note 3						
Gate Threshold Voltage	$V_{GS} = V_{DS}$ , $I_{D} = 250 \mu A$	$V_{GS(th)}$	0.4		1.4	V
Static Drain-Source On-Resistance	$V_{GS} = 10V$ , $I_D = 6A$	R <sub>DS(ON)</sub>		98	120	mΩ
Static Drain-Source On-Resistance	$V_{GS} = 5V$ , $I_D = 6A$	R <sub>DS(ON)</sub>		103	130	mΩ
Static Drain-Source On-Resistance	$V_{GS} = 3V$ , $I_D = 3A$	R <sub>DS(ON)</sub>		120	165	mΩ
Dynamic Characteristics Note 3						
Input Capacitance	$V_{DS} = 25V$ , $V_{GS} = 0V$ , $f = 1MHz$	C <sub>ISS</sub>		520		pF
Output Capacitance	$V_{DS} = 25V$ , $V_{GS} = 0V$ , $f = 1MHz$	Coss		120		pF
Reverse Transfer Capacitance	$V_{DS} = 25V$ , $V_{GS} = 0V$ , $f = 1MHz$	$C_{RSS}$		35		pF
Switching Characteristics Note 3						
Turn-On Delay Time	$V_{DD} = 50V$ , $V_{GS} = 10V$ , $I_D = 11A$ , $R_{G(ext)} = 6\Omega$	t <sub>D(ON)</sub>		8		ns
Turn-On Rise Time	$V_{DD} = 50V$ , $V_{GS} = 10V$ , $I_D = 11A$ , $R_{G(ext)} = 6\Omega$	$t_R$		4		ns
Turn-Off Delay Time	$V_{DD} = 50V$ , $V_{GS} = 10V$ , $I_D = 11A$ , $R_{G(ext)} = 6\Omega$	t <sub>D(OFF)</sub>		34		ns
Turn-Off Fall Time	$V_{DD}$ = 50V, $V_{GS}$ = 10V, $I_D$ = 11A, $R_{G(ext)}$ = $6\Omega$	t <sub>F</sub>		5		ns
Total Gate Charge	$V_{DS} = 80V$ , $V_{GS} = 4.5V$ , $I_D = 11A$	$Q_{G}$		9.7		nC
Gate Source Charge	$V_{DS} = 80V$ , $V_{GS} = 4.5V$ , $I_{D} = 11A$	Q <sub>GS</sub>		0.9		nC
Gate Drain Charge	$V_{DS} = 80V$ , $V_{GS} = 4.5V$ , $I_{D} = 11A$	$Q_{GD}$		5.3		nC
Drain-Source Diode Characteristics and Maximum Ratings						
Drain-Source Diode Forward Current		Is			12	А
Drain-Source Diode Forward Voltage Note 2	V <sub>GS</sub> = 0V, I <sub>S</sub> = 12A	$V_{SD}$			1.5	V

#### Notes

- 1: Repetitive Rating: Pulse width limited by maximum junction temperature
- 2: Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 3: Guaranteed by design, not subject to production testing.
- 4: Pulse width limited by safe operating area.
- 5: L = 0.5mH,  $I_{AS} = 12$ A,  $V_{DD} = 25$ V,  $R_G = 25$ Ω, Starting  $T_J = 25$ °C



#### REFERENCE DATA A TYPICAL DEVICE PERFORMANCE



O 2 4 6 8 10

Fig. 2 • Transfer Characteristics

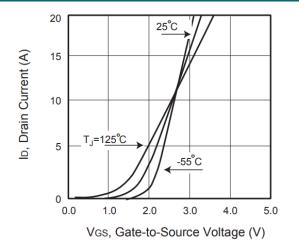


Fig. 3 • Capacitance

VDS, Drain-to-Source Voltage (V)

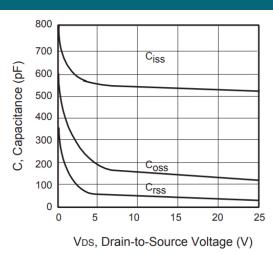


Fig. 4 • On-Resistance Variation with Temperature

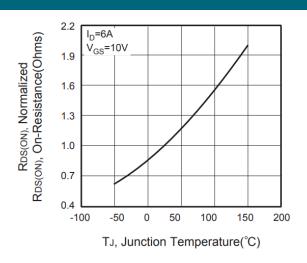


Fig. 5 • Gate Threshold Variation with Temperature

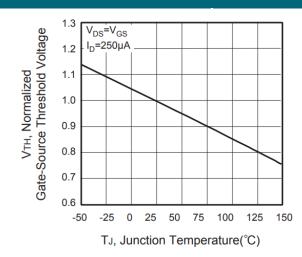
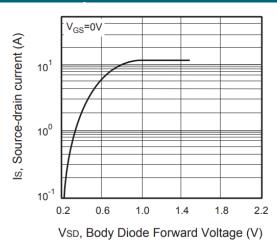


Fig. 6 • Body Diode Forward Voltage Variation with Source Current





#### REFERENCE DATA A TYPICAL DEVICE PERFORMANCE



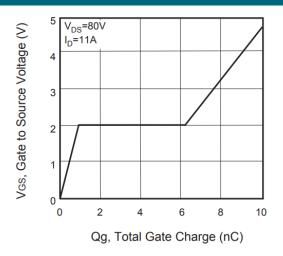


Fig. 8 • Maximum Safe Operating Area

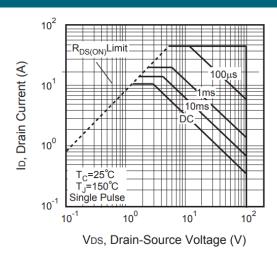
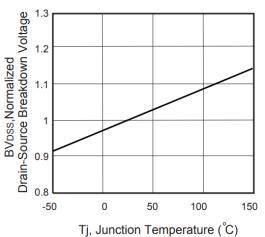
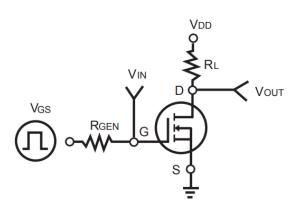


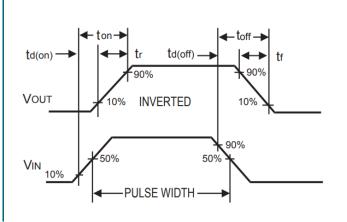
Fig. 9 • Breakdown Voltage Variation vs. Temperature







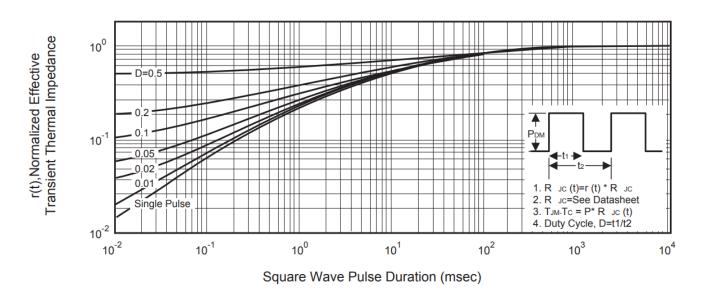






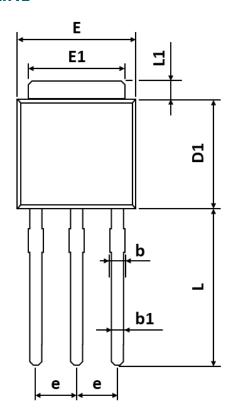
## REFERENCE DATA A TYPICAL DEVICE PERFORMANCE

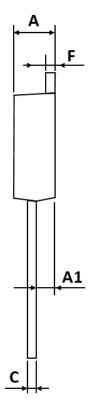
## Fig. 12 • Normalized Thermal Transient Impedance Curve





# **PACKAGE OUTLINE**





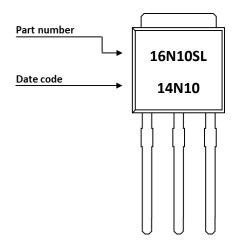
Sym	Millimeters (Min.)	Millimeters (Typ.)	Millimeters (Max.)
Α	2.180	-	2.400
A1	0.860	-	1.500
b	0.700	-	0.960
b1	0.700	-	0.860
С	0.400	-	0.610
D1	5.400	-	6.630
Е	6.050	-	7.010
E1	4.950	-	5.460
е	1.980	-	2.590
F	0.400	-	0.890
L	8.500	-	9.650
L1	0.500	-	1.800

# **ORDERING INFORMATION**

Part Number	Package	Packing	Tube Qty.	Inner Box Qty.	Outer Box Qty.
CED16N10SL	TO251 (E-PAK)	Tube	80pcs	4,000pcs	16,000pcs

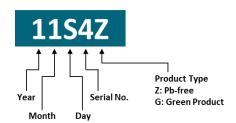


#### **PART MARKING**



#### **DATE CODE**

Example: 11S4Z



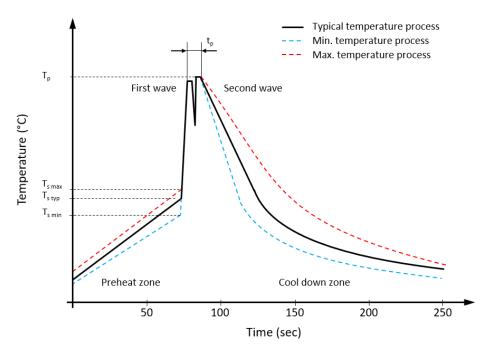


Coding list for "Day"





## RECOMMENDED WAVE SOLDERING PROFILE A THT PACKAGE



# Classification wave soldering profile ▲ Refer to EN 61760-1: 2006

Profile Features		Value ▲ Sn-Pb Assembly	Value ▲ Pb-free Assembly
Preheat temperature min.	$T_{s  min}$	100 °C	100 °C
Preheat temperature typical	T <sub>s typ</sub>	120 °C	120 °C
Preheat temperature max.	T <sub>s max</sub>	130 °C	130 °C
Preheat time $t_s$ from $T_{smin}$ to $T_{smax}$	ts	70 seconds	70 seconds
Peak temperature	Tp	235 °C to 260 °C	245 °C to 260 °C
Time of actual peak temperature	t <sub>p</sub>	Max. 10 seconds Max. 5 second each wave	Max. 10 seconds Max. 5 second each wave
Ramp-down date min.		~ 2 °C/second	~ 2 °C/second
Ramp-down rate typical		~ 3.5 °C/second	~ 3.5 °C/second
Ramp-down rate max.		~ 5 °C/second	~ 5 °C/second
Time 25°C to 25°C		4 minutes	4 minutes



#### **REVISION TABLE**

Revision	Date	Status	Notes
001	30/09/2022	Initial release	Initial publication

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