SILICON (Si) POWER MOSFET A CED540L



CET MOS

CED540L

100V ▲ 40mΩ ▲ 25A ▲ Si MOSFET

SILICON Si MOSFET ▲ THT type N-channel enhancement mode UL94V-0 rated flame retardant epoxy TO251 (E-PAK) package Super high dense cell density for extremely low R_{DS(ON)} High power and current handling capability

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HALOGEN

FREE

RoHS



Parameter (T _c = 25°C, unless otherwise noted)	Characteristics	
Drain-Source Voltage	V _{DS}	100V
Gate-Source Voltage	V _{GS}	±20V
Continuous Drain Current at T _c = 25°C	I _D	25A
Pulsed Drain Current Note 1	I _{DM} Note4	100A
Maximum Power Dissipation at T _c = 25°C	PD	56W
Power Dissipation Derating above 25°C	ΔP _D	0.45W/°C
Operating and Storage Temperature Range	T _J , T _{STG}	-55°C to +175°C

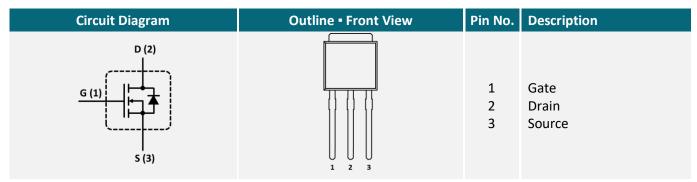
THERMAL CHARACTERISTICS

Parameter	Symbol	Limit
Thermal Resistance, Junction-to-Case	R _{TH_JC}	2.2°C/W
Thermal Resistance, Junction-to-Ambient Note 2	R _{TH_JA}	50°C/W

APPLICATIONS

Battery Management Systems	E-Bike	Industrial Control	Power Inverter	UPS
+ 4 -	50			

PIN DESCRIPTION



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ELECTRICAL CHARACTERISTICS A T_c = 25°C, unless otherwise noted

Item	Condition	Symbol	Min.	Тур.	Max.	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	$V_{GS} = 0V$, $I_D = 250\mu A$	BV _{DSS}	100			V
Zero Gate Voltage Drain Current	V_{DS} = 100V, V_{GS} = 0V	I _{DSS}			25	μA
Gate Body Leakage Current, Forward	$V_{GS} = 20V, V_{DS} = 0V$	I _{GSSF}			100	nA
Gate Body Leakage Current, Reverse	V_{GS} = -20V, V_{DS} = 0V	I _{GSSR}			-100	nA
On Characteristics Note 3						
Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$	V _{GS(th)}	1		3	V
Static Drain-Source On-Resistance	V _{GS} = 10V, I _D = 18A	R _{DS(ON)}		40	50	mΩ
Static Drain-Source On-Resistance	V _{GS} = 5V, I _D = 15A	R _{DS(ON)}		43	53	mΩ
Dynamic Characteristics Note 3						
Input Capacitance	V_{DS} = 25V, V_{GS} = 0V, f = 1MHz	CISS		1295		рF
Output Capacitance	$V_{DS} = 25V, V_{GS} = 0V, f = 1MHz$	Coss		199		рF
Reverse Transfer Capacitance	$V_{DS} = 25V, V_{GS} = 0V, f = 1MHz$	C _{RSS}		40		рF
Forward Transconductance	$V_{DS} = 25V, I_{D} = 18A$	g _{FS}		14		S
Switching Characteristics Note 3						
Turn-On Delay Time	V_{DD} = 50V, V_{GS} = 10V, I_{D} = 18A, $R_{\text{G(ext)}}$ = 5.1 Ω	t _{D(ON)}		13	26	ns
Turn-On Rise Time	V_{DD} = 50V, V_{GS} = 10V, I_{D} = 18A, $R_{\text{G(ext)}}$ = 5.1 Ω	t _R		3.1	7	ns
Turn-Off Delay Time	V_{DD} = 50V, V_{GS} = 10V, I_{D} = 18A, $R_{\text{G(ext)}}$ = 5.1 Ω	t _{D(OFF)}		55	110	ns
Turn-Off Fall Time	V_{DD} = 50V, V_{GS} = 10V, I_{D} = 18A, $R_{\text{G}(\text{ext})}$ = 5.1 Ω	t _F		5	10	ns
Total Gate Charge	V_{DS} = 80V, V_{GS} = 10V, I_{D} = 18A	Q _G		40	80	nC
Gate Source Charge	V_{DS} = 80V, V_{GS} = 10V, I_{D} = 18A	Q _{GS}		3.7		nC
Gate Drain Charge	V_{DS} = 80V, V_{GS} = 10V, I_D = 18A	\mathbf{Q}_{GD}		10		nC
Drain-Source Diode Characteristics a	nd Maximum Ratings					
Drain-Source Diode Forward Current ^{Note 2}		ls			25	А
Drain-Source Diode Forward Voltage Note 2	V _{GS} = 0V, I _S = 18A	V_{SD}			1.3	V

Notes

- 1: Repetitive Rating: Pulse width limited by maximum junction temperature
- 2: Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- 3: Guaranteed by design, not subject to production testing.
- 4: Pulse width limited by safe operating area.
- 5: L = 1mH, $I_{AS} = 15A$, $V_{DD} = 50V$, $R_G = 25\Omega$, Starting $T_J = 25^{\circ}C$

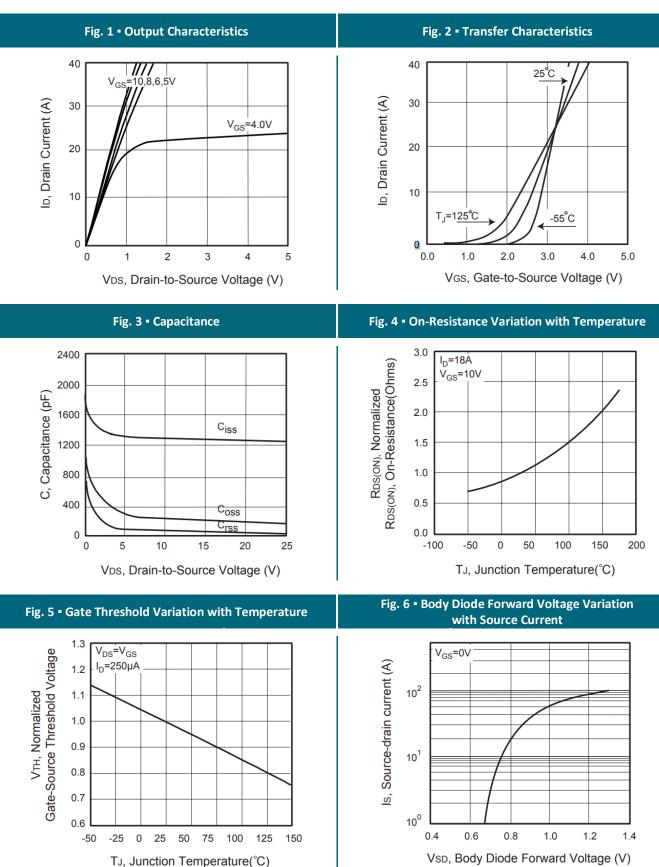


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REFERENCE DATA A TYPICAL DEVICE PERFORMANCE





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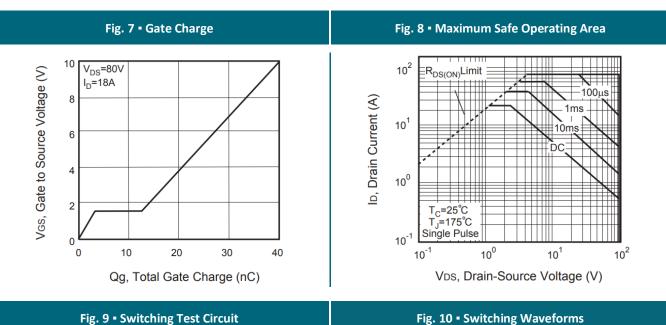
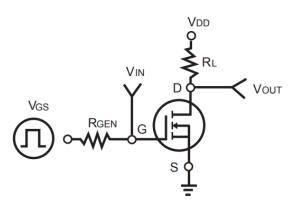


Fig. 9 - Switching Test Circuit



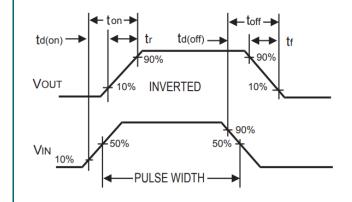
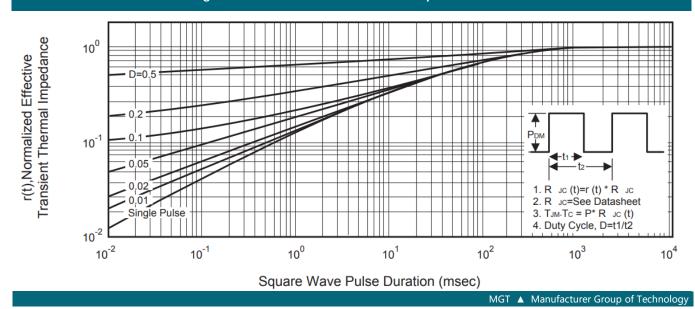


Fig. 11 • Normalized Thermal Transient Impedance Curve

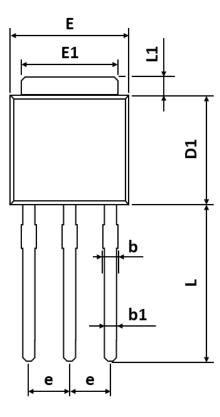


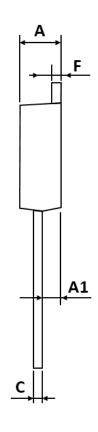
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PACKAGE OUTLINE





Sym	Millimeters (Min.)	Millimeters (Typ.)	Millimeters (Max.)
А	2.180	-	2.400
A1	0.860	-	1.500
b	0.700	-	0.960
b1	0.700	-	0.860
С	0.400	-	0.610
D1	5.400	-	6.630
E	6.050	-	7.010
E1	4.950	-	5.460
е	1.980	-	2.590
F	0.400	-	0.890
L	8.500	-	9.650
L1	0.500	-	1.800

ORDERING INFORMATION

Part Number	Package	Packing	Tube Qty.	Inner Box Qty.	Outer Box Qty.
CED540L	TO251 (E-PAK)	Tube	80pcs	4,000pcs	16,000pcs

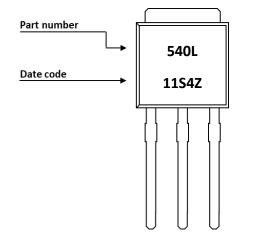
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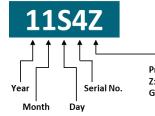


PART MARKING



DATE CODE

Example: 11S4Z



| Product Type Z: Pb-free G: Green Product

	Coding list for "Day"								
1	2	3	4	5	6	7	8	9	A
01	02	03	04	05	06	07	08	09	10
B	C	D	E	F	G	H	┃	J	K
11	12	13	14	15	16	17	18	19	20
L	M	N	O	P	Q	R	S	T	U
21	22	23	24	25	26	27	28	29	30
V 31									

Coding list for "Month"

1 Jan	2 Feb			5 May	
7	8	9	A	B	C
Jul	Aug	Sep	Oct	Nov	Dec

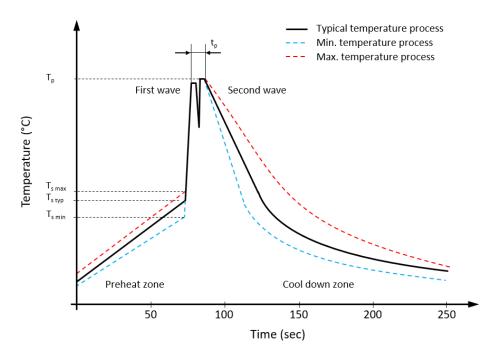
Coding list for "Year"







RECOMMENDED WAVE SOLDERING PROFILE ▲ THT PACKAGE



Classification wave soldering profile ▲ Refer to EN 61760-1: 2006

Profile Features		Value 🛦 Sn-Pb Assembly	Value 🔺 Pb-free Assembly
Preheat temperature min.	$T_{s min}$	100 °C	100 °C
Preheat temperature typical	T _{s typ}	120 °C	120 °C
Preheat temperature max.	$T_{s max}$	130 °C	130 °C
Preheat time t_s from $T_{s min}$ to $T_{s max}$	ts	70 seconds	70 seconds
Peak temperature	Tp	235 °C to 260 °C	245 °C to 260 °C
Time of actual peak temperature	t _p	Max. 10 seconds Max. 5 second each wave	Max. 10 seconds Max. 5 second each wave
Ramp-down date min.		~ 2 °C/second	~ 2 °C/second
Ramp-down rate typical		~ 3.5 °C/second	~ 3.5 °C/second
Ramp-down rate max.		~ 5 °C/second	~ 5 °C/second
Time 25°C to 25°C		4 minutes	4 minutes

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REVISION TABLE

Revision	Date	Status	Notes
001	30/09/2022	Initial release	Initial publication

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