









CED6186A

60V Δ 14mΩ Δ 30.6A Δ Si MOSFET

SILICON Si MOSFET ▲ THT type
N-channel enhancement mode
UL94V-0 rated flame retardant epoxy
TO251 (E-PAK) package

Super high dense cell density for extremely low R_{DS(ON)} **High power and current handling capability**

MAXIMUM RATINGS

Parameter (T_c = 25°C, unless otherwise noted)		Characteristics
Drain-Source Voltage	V _{DS}	60V
Gate-Source Voltage	V _{GS}	±20V
Continuous Drain Current at T _C = 25°C	I _D	30.6A
Continuous Drain Current at T _C = 100°C	I _D	21.7A
Pulsed Drain Current Note 1	I _{DM}	122.4A
Maximum Power Dissipation at T _C = 25°C	P _D	38W
Power Dissipation Derating above 25°C	ΔP_D	0.25W/°C
Single Pulsed Avalanche Energy Note 4	E _{AS}	50mJ
Single Pulsed Avalanche Current Note 4	I _{AS}	10A
Operating and Storage Temperature Range	T _J , T _{STG}	-55°C to +175°C

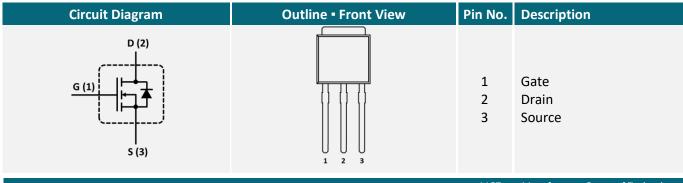
THERMAL CHARACTERISTICS

Parameter	Symbol	Limit
Thermal Resistance, Junction-to-Case	R _{TH_JC}	4°C/W
Thermal Resistance, Junction-to-Ambient	R _{TH JA}	50°C/W

APPLICATIONS

Battery Management Systems	DC/DC	DC	Industrial	Power
	Converter	Fan	Control	Switches
+4-				

PIN DESCRIPTION





ELECTRICAL CHARACTERISTICS ▲ T_C = 25°C, unless otherwise noted

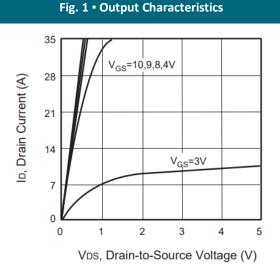
Item	Condition	Symbol	Min.	Тур.	Max.	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	$V_{GS} = 0V$, $I_D = 250\mu A$	BV_DSS	60			V
Zero Gate Voltage Drain Current	$V_{DS} = 60V, V_{GS} = 0V$	I _{DSS}			1	μΑ
Gate Body Leakage Current, Forward	$V_{GS} = 20V, V_{DS} = 0V$	I_{GSSF}			100	nA
Gate Body Leakage Current, Reverse	$V_{GS} = -20V, V_{DS} = 0V$	I_{GSSR}			-100	nA
On Characteristics Note 2						
Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_{D} = 250 \mu A$	$V_{GS(th)}$	1		3	V
Static Drain-Source On-Resistance	$V_{GS} = 10V, I_D = 15A$	R _{DS(ON)}		14	20	mΩ
Static Drain-Source On-Resistance	$V_{GS} = 4.5V$, $I_D = 7.5A$	R _{DS(ON)}		18	25	mΩ
Dynamic Characteristics Note 3						
Input Capacitance	$V_{DS} = 25V$, $V_{GS} = 0V$, $f = 1MHz$	C _{ISS}		780		pF
Output Capacitance	$V_{DS} = 25V$, $V_{GS} = 0V$, $f = 1MHz$	Coss		115		pF
Reverse Transfer Capacitance	$V_{DS} = 25V$, $V_{GS} = 0V$, $f = 1MHz$	C _{RSS}		95		pF
Switching Characteristics Note 3						
Turn-On Delay Time	V_{DD} = 30V, V_{GS} = 10V, I_D = 20A, $R_{G(ext)}$ = 4.7 Ω	t _{D(ON)}		14		ns
Turn-On Rise Time	V_{DD} = 30V, V_{GS} = 10V, I_D = 20A, $R_{G(ext)}$ = 4.7 Ω	t_R		6		ns
Turn-Off Delay Time	V_{DD} = 30V, V_{GS} = 10V, I_{D} = 20A, $R_{G(ext)}$ = 4.7 Ω	$t_{D(OFF)}$		37		ns
Turn-Off Fall Time	V_{DD} = 30V, V_{GS} = 10V, I_{D} = 20A, $R_{G(ext)}$ = 4.7 Ω	$t_{\scriptscriptstyle{F}}$		9		ns
Total Gate Charge	$V_{DS} = 48V$, $V_{GS} = 4.5V$, $I_{D} = 20A$	Q_{G}		13		nC
Gate Source Charge	$V_{DS} = 48V$, $V_{GS} = 4.5V$, $I_{D} = 20A$	Q_{GS}		2		nC
Gate Drain Charge	$V_{DS} = 48V$, $V_{GS} = 4.5V$, $I_{D} = 20A$	Q_{GD}		10		nC
Drain-Source Diode Characteristics and Maximum Ratings						
Drain-Source Diode Forward Current		Is			30.6	Α
Drain-Source Diode Forward Voltage Note 2	$V_{GS} = 0V$, $I_S = 30.6A$	V_{SD}			1.2	V

Notes

- 1: Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2: Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 3: Guaranteed by design, not subject to production testing.
- 4: L = 1mH, $I_{AS} = 10A$, $V_{DD} = 25V$, $R_G = 25\Omega$, Starting $T_J = 25^{\circ}C$



REFERENCE DATA A TYPICAL DEVICE PERFORMANCE



50 25°C 40 40 T_J=1<u>25°C -55°C </u>
0 0 2 4 6 8

Vgs, Gate-to-Source Voltage (V)

Fig. 2 • Transfer Characteristics

1200 1000 C, Capacitance (pF) Ciss 800 600 400 200 Coss C_{rss} 0 10 15 20 25 0

Fig. 3 • Capacitance

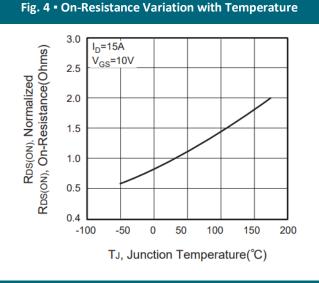


Fig. 5 • Gate Threshold Variation with Temperature

VDS, Drain-to-Source Voltage (V)

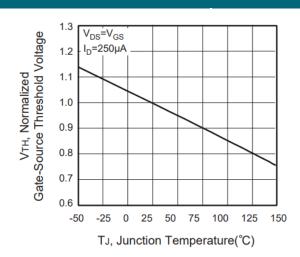
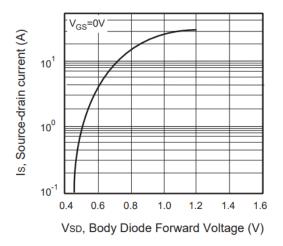


Fig. 6 • Body Diode Forward Voltage Variation with Source Current





REFERENCE DATA A TYPICAL DEVICE PERFORMANCE

Fig. 7 • Gate Charge

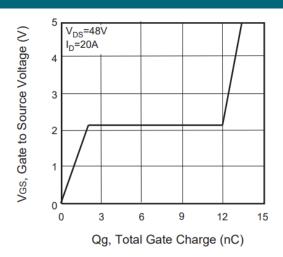


Fig. 8 • Maximum Safe Operating Area

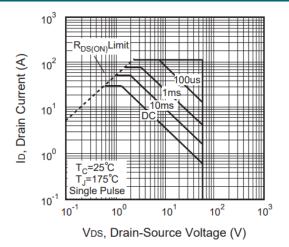


Fig. 9 • Breakdown Voltage Variation vs. Temperature

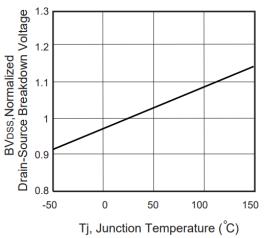
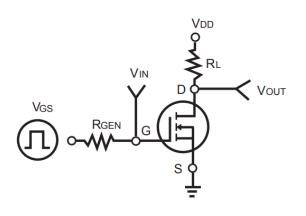
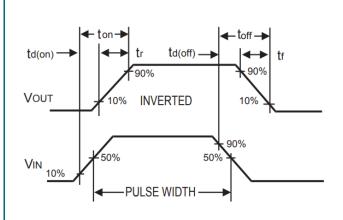


Fig. 10 • Switching Test Circuit



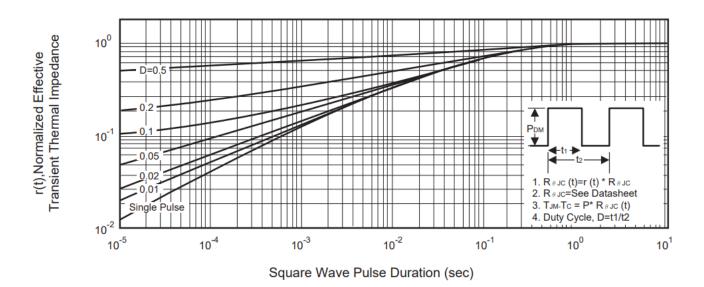






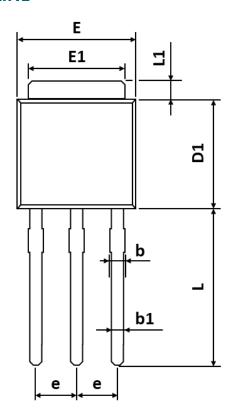
REFERENCE DATA A TYPICAL DEVICE PERFORMANCE

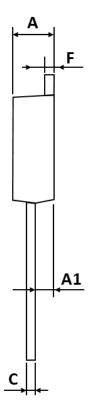
Fig. 12 • Normalized Thermal Transient Impedance Curve





PACKAGE OUTLINE





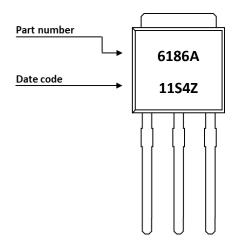
Sym	Millimeters (Min.)	Millimeters (Typ.)	Millimeters (Max.)
Α	2.180	-	2.400
A1	0.860	-	1.500
b	0.700	-	0.960
b1	0.700	-	0.860
С	0.400	-	0.610
D1	5.400	-	6.630
Е	6.050	-	7.010
E1	4.950	-	5.460
е	1.980	-	2.590
F	0.400	-	0.890
L	8.500	-	9.650
L1	0.500	-	1.800

ORDERING INFORMATION

Part Numb	er Package	Packing	Tube Qty.	Inner Box Qty.	Outer Box Qty.
CED6186A	TO251 (E-PAK)	Tube	80pcs	4,000pcs	16,000pcs



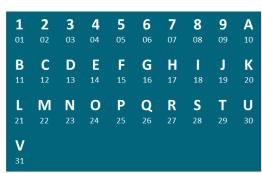
PART MARKING



DATE CODE

Example: 11S4Z



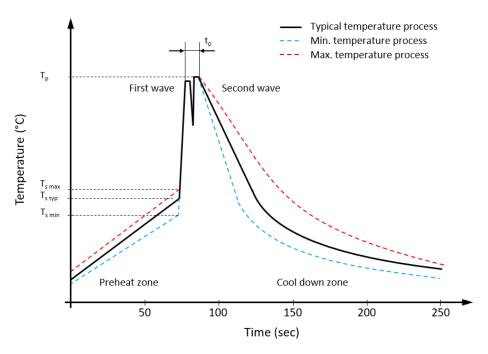


Coding list for "Day"





RECOMMENDED WAVE SOLDERING PROFILE A THT PACKAGE



Classification wave soldering profile ▲ Refer to EN 61760-1: 2006

Profile Features		Value ▲ Sn-Pb Assembly	Value ▲ Pb-free Assembly
Preheat temperature min.	$T_{s min}$	100 °C	100 °C
Preheat temperature typical	T _{s typ}	120 °C	120 °C
Preheat temperature max.	T _{s max}	130 °C	130 °C
Preheat time t_s from T_{smin} to T_{smax}	ts	70 seconds	70 seconds
Peak temperature	Tp	235 °C to 260 °C	245 °C to 260 °C
Time of actual peak temperature	t _p	Max. 10 seconds Max. 5 second each wave	Max. 10 seconds Max. 5 second each wave
Ramp-down date min.		~ 2 °C/second	~ 2 °C/second
Ramp-down rate typical		~ 3.5 °C/second	~ 3.5 °C/second
Ramp-down rate max.		~ 5 °C/second	~ 5 °C/second
Time 25°C to 25°C		4 minutes	4 minutes



REVISION TABLE

Revision	Date	Status	Notes
001	30/09/2022	Initial release	Initial publication

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