









CEF18N5A

500V Δ 0.24Ω Δ 18ANote 4 Δ Si MOSFET

SILICON Si MOSFET ▲ THT type
N-channel enhancement mode
UL94V-0 rated flame retardant epoxy

TO220F-3L package ▲ Electrical insulated mounting tab

Super high dense cell density for extremely low R_{DS(ON)}

High power and current handling capability

MAXIMUM RATINGS

Parameter (T _C = 25°C, unless otherwise noted)		Characteristics
Drain-Source Voltage	V _{DS}	650V
Gate-Source Voltage	V _{GS}	±30V
Continuous Drain Current at T _C = 25°C	l _D	18A Note 4
Continuous Drain Current at T _C = 100°C	l _D	11A Note 4
Pulsed Drain Current Note 1	I _{DM} Note 5	72A Note 4
Maximum Power Dissipation at $T_c = 25^{\circ}C$	P _D	66W
Power Dissipation Derating above 25°C	ΔP_D	0.5W/°C
Single Pulsed Avalanche Energy Note 6	E _{AS}	859mJ
Single Pulsed Avalanche Current Note 6	l _{AS}	18
Operating and Storage Temperature Range	T _J , T _{STG}	-55°C to +150°C

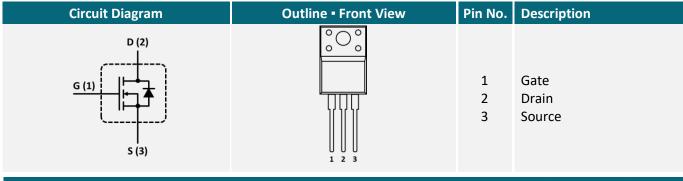
THERMAL CHARACTERISTICS

Parameter	Symbol	Limit
Thermal Resistance, Junction-to-Case	R _{TH_JC}	1.9°C/W
Thermal Resistance, Junction-to-Ambient	R _{TH JA}	65°C/W

APPLICATIONS

General Lighting LED & CCFL	Industrial Inverters	Motors & Drives	Power Supplies	UPS

PIN DESCRIPTION





ELECTRICAL CHARACTERISTICS ▲ T_C = 25°C, unless otherwise noted

Item	Condition	Symbol	Min.	Тур.	Max.	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	$V_{GS} = 0V$, $I_D = 250\mu A$	BV_DSS	500			V
Zero Gate Voltage Drain Current	$V_{DS} = 500V, V_{GS} = 0V$	I _{DSS}			1	μΑ
Gate Body Leakage Current, Forward	$V_{GS} = 30V, V_{DS} = 0V$	I_{GSSF}			100	nA
Gate Body Leakage Current, Reverse	$V_{GS} = -30V, V_{DS} = 0V$	I _{GSSR}			-100	nA
On Characteristics Note 2						
Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 250 \mu A$	$V_{GS(th)}$	2		4	V
Static Drain-Source On-Resistance	$V_{GS} = 10V, I_D = 9A$	R _{DS(ON)}		0.24	0.27	Ω
Dynamic Characteristics Note 3						
Input Capacitance	$V_{DS} = 25V$, $V_{GS} = 0V$, $f = 1MHz$	C _{ISS}		2465		pF
Output Capacitance	$V_{DS} = 25V$, $V_{GS} = 0V$, $f = 1MHz$	Coss		300		pF
Reverse Transfer Capacitance	$V_{DS} = 25V$, $V_{GS} = 0V$, $f = 1MHz$	C_{RSS}		10		pF
Switching Characteristics Note 3						
Turn-On Delay Time	V_{DD} = 250V, V_{GS} = 10V, I_D = 18A, $R_{G(ext)}$ = 25 Ω	t _{D(ON)}		36		ns
Turn-On Rise Time	V_{DD} = 250V, V_{GS} = 10V, I_D = 18A, $R_{G(ext)}$ = 25 Ω	t_R		28		ns
Turn-Off Delay Time	V_{DD} = 250V, V_{GS} = 10V, I_D = 18A, $R_{G(ext)}$ = 25 Ω	t _{D(OFF)}		78		ns
Turn-Off Fall Time	V_{DD} = 250V, V_{GS} = 10V, I_D = 18A, $R_{G(ext)}$ = 25 Ω	t _F		11		ns
Total Gate Charge	$V_{DS} = 400V$, $V_{GS} = 10V$, $I_D = 18A$	Q_{G}		58		nC
Gate Source Charge	$V_{DS} = 400V$, $V_{GS} = 10V$, $I_D = 18A$	Q_{GS}		11		nC
Gate Drain Charge	V_{DS} = 400V, V_{GS} = 10V, I_D = 18A	Q_{GD}		23		nC
Drain-Source Diode Characteristics and Maximum Ratings						
Drain-Source Diode Forward Current		I _S			10	Α
Drain-Source Diode Forward Voltage Note 2	V _{GS} = 0V, I _S = 10A	V_{SD}			1.4	V

Notes

- 1: Repetitive Rating: Pulse width limited by maximum junction temperature
- 2: Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 3: Guaranteed by design, not subject to production testing.
- 4: Limited only by maximum temperature allowed.
- 5: Pulse width limited by safe operating area.
- 6: L = 5.3mH, I_{AS} = 18A, V_{DD} = 50V, R_G = 25Ω, Starting T_J = 25°C



REFERENCE DATA A TYPICAL DEVICE PERFORMANCE



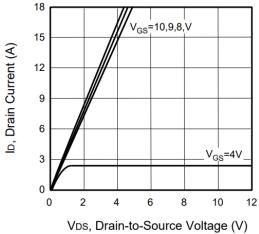


Fig. 2 • Transfer Characteristics

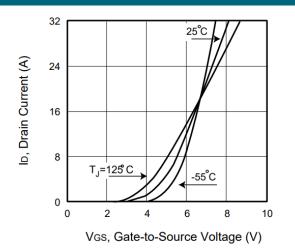


Fig. 3 • Capacitance

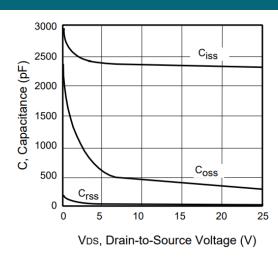


Fig. 4 • On-Resistance Variation with Temperature

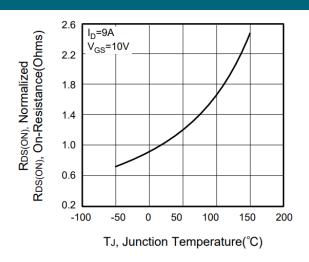


Fig. 5 • Gate Threshold Variation with Temperature

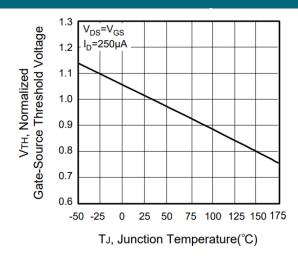
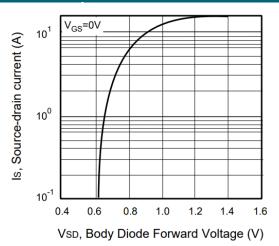


Fig. 6 • Body Diode Forward Voltage Variation with Source Current



MGT A Manufacturer Group of Technology



REFERENCE DATA A TYPICAL DEVICE PERFORMANCE

Fig. 7 • Gate Charge

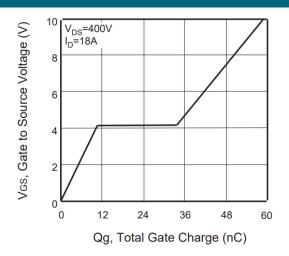


Fig. 8 • Maximum Safe Operating Area

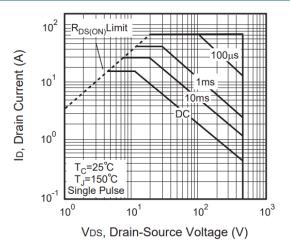
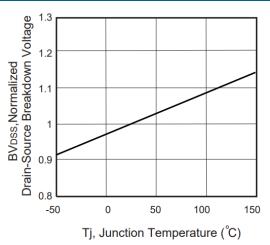


Fig. 9 • Breakdown Voltage Variation vs. Temperature



rig. 5 Dicardown Voltage Variation V3. Temperature



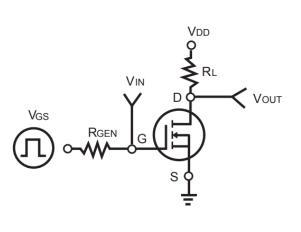
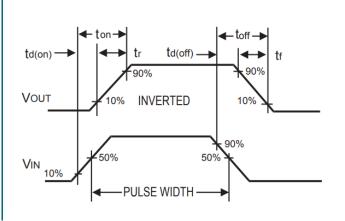


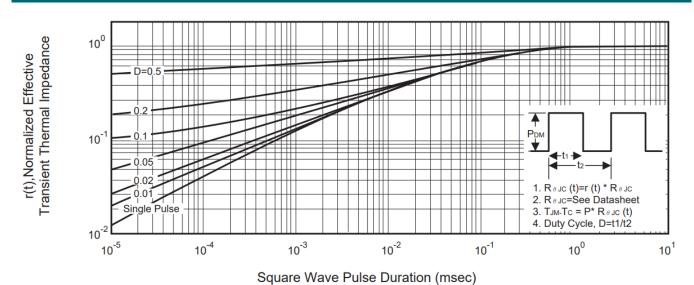
Fig. 11 • Switching Waveforms





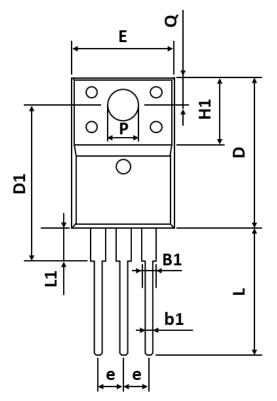
REFERENCE DATA A TYPICAL DEVICE PERFORMANCE

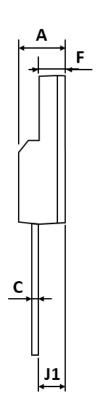
Fig. 12 • Normalized Thermal Transient Impedance Curve





PACKAGE OUTLINE





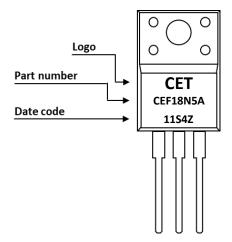
Sym	Millimeters (Min.)	Millimeters (Typ.)	Millimeters (Max.)
Α	4.500	-	5.000
B1	1.000	-	1.500
b1	0.700	-	0.950
С	0.420	-	0.700
D	15.670	-	16.070
D1	14.800	-	16.000
E	9.960	-	10.360
e	2.340	-	2.740
F	2.340	-	2.740
H1	6.480	-	6.900
J1	2.550	-	2.950
L	12.080	-	13.480
L1	2.230	-	3.650
Q	3.100	-	3.500
Р	2.980	-	3.380

ORDERING INFORMATION

Part Number	Package	Packing	Tube Qty.	Inner Box Qty.	Outer Box Qty.
CEF18N5A	TO-220F-3L	Tube	50pcs	1,000pcs	4,000pcs

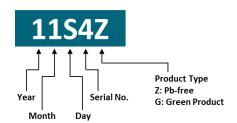


PART MARKING



DATE CODE

Example: 11S4Z



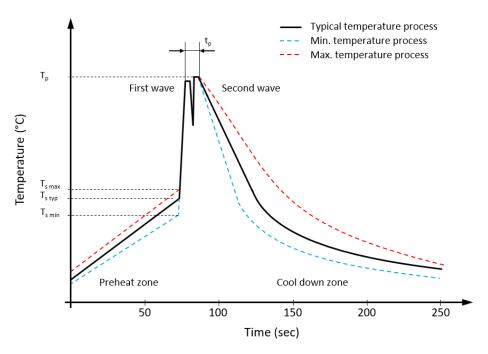


Coding list for "Day"





RECOMMENDED WAVE SOLDERING PROFILE A THT PACKAGE



Classification wave soldering profile ▲ Refer to EN 61760-1: 2006

Profile Features		Value ▲ Sn-Pb Assembly	Value ▲ Pb-free Assembly
Preheat temperature min.	T_{smin}	100 °C	100 °C
Preheat temperature typical	T _{s typ}	120 °C	120 °C
Preheat temperature max.	T_{smax}	130 °C	130 °C
Preheat time t_s from T_{smin} to T_{smax}	t_s	70 seconds	70 seconds
Peak temperature	T_p	235 °C to 260 °C	245 °C to 260 °C
Time of actual peak temperature	t _p	Max. 10 seconds Max. 5 second each wave	Max. 10 seconds Max. 5 second each wave
Ramp-down date min.		~ 2 °C/second	~ 2 °C/second
Ramp-down rate typical		~ 3.5 °C/second	~ 3.5 °C/second
Ramp-down rate max.		~ 5 °C/second	~ 5 °C/second
Time 25°C to 25°C		4 minutes	4 minutes



REVISION TABLE

Revision	Date	Status	Notes
001	30/09/2022	Initial release	Initial publication

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