SILICON (Si) POWER MOSFET A CEF60N15



CET MOS

CEF60N15

150V A 13.2mΩ A 60ANote 4 A Si MOSFET

SILICON Si MOSFET ▲ THT type N-channel enhancement mode UL94V-0 rated flame retardant epoxy TO220F-3L package ▲ Electrical insulated mounting tab Super high dense cell density for extremely low R_{DS(ON)} High power and current handling capability







MAXIMUM RATINGS

Parameter (T _c = 25°C, unless otherwise noted)		Characteristics
Drain-Source Voltage	V _{DS}	150V
Gate-Source Voltage	V _{GS}	±20V
Continuous Drain Current at T _c = 25°C	Ι _D	60A Note 4
Continuous Drain Current at T _c = 100°C	Ι _D	38A Note 4
Pulsed Drain Current Note 1	IDM Note 5	240A Note 4
Maximum Power Dissipation at $T_c = 25^{\circ}C$	PD	35W
Power Dissipation Derating above 25°C	ΔΡ _D	0.28W/°C
Single Pulsed Avalanche Energy Note 6	E _{AS}	31.25mJ
Single Pulsed Avalanche Current Note 6	I _{AS}	25A
Operating and Storage Temperature Range	T _J , T _{STG}	-55°C to +150°C

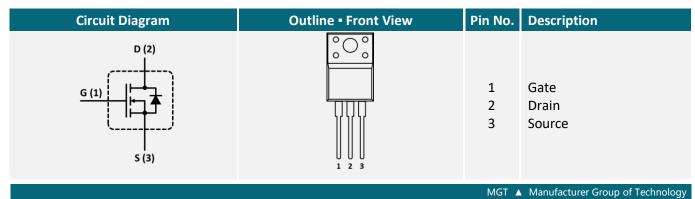
THERMAL CHARACTERISTICS

Parameter	Symbol	Limit
Thermal Resistance, Junction-to-Case	R _{TH_JC}	3.6°C/W
Thermal Resistance, Junction-to-Ambient	R _{th_ja}	65°C/W

APPLICATIONS

Battery Management Systems	E-Bike	Industrial Control	Power Inverter	UPS
+ 4 -	50			

PIN DESCRIPTION



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ELECTRICAL CHARACTERISTICS A T_c = 25°C, unless otherwise noted

ltem	Condition	Symbol	Min.	Тур.	Max.	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	$V_{GS} = 0V$, $I_D = 250\mu A$	BV _{DSS}	150			V
Zero Gate Voltage Drain Current	V_{DS} = 150V, V_{GS} = 0V	I _{DSS}			1	μΑ
Gate Body Leakage Current, Forward	$V_{GS} = 20V$, $V_{DS} = 0V$	I _{GSSF}			100	nA
Gate Body Leakage Current, Reverse	V_{GS} = -20V, V_{DS} = 0V	I _{GSSR}			-100	nA
On Characteristics Note 2						
Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 250 \mu A$	V _{GS(th)}	2		4	V
Static Drain-Source On-Resistance	$V_{GS} = 10V, I_D = 20A$	R _{DS(ON)}		13.2	17	mΩ
Dynamic Characteristics Note 3						
Input Capacitance	V_{DS} = 75V, V_{GS} = 0V, f = 1MHz	CISS		1920		рF
Output Capacitance	V_{DS} = 75V, V_{GS} = 0V, f = 1MHz	Coss		225		рF
Reverse Transfer Capacitance	V_{DS} = 75V, V_{GS} = 0V, f = 1MHz	C _{RSS}		15		pF
Switching Characteristics Note 3						
Turn-On Delay Time	V_{DD} = 75V, V_{GS} = 10V, I_{D} = 20A, $R_{\text{G(ext)}}$ = 10 Ω	t _{D(ON)}		25		ns
Turn-On Rise Time	V_{DD} = 75V, V_{GS} = 10V, I_{D} = 20A, $R_{\text{G(ext)}}$ = 10 Ω	t _R		6		ns
Turn-Off Delay Time	V_{DD} = 75V, V_{GS} = 10V, I_{D} = 20A, $R_{\text{G(ext)}}$ = 10 Ω	t _{D(OFF)}		38		ns
Turn-Off Fall Time	V_{DD} = 75V, V_{GS} = 10V, I_{D} = 20A, $R_{\text{G(ext)}}$ = 10 Ω	t _F		7		ns
Total Gate Charge	V_{DS} = 75V, V_{GS} = 10V, I_{D} = 20A	Q_{G}		30		nC
Gate Source Charge	V_{DD} = 75V, V_{GS} = 10V, I_D = 20A	Q _{GS}		9		nC
Gate Drain Charge	V_{DD} = 75V, V_{GS} = 10V, I_D = 20A	\mathbf{Q}_{GD}		6		nC
Drain-Source Diode Characteristics a	nd Maximum Ratings					
Drain-Source Diode Forward Current		I _S			34.7	A
Drain-Source Diode Forward Voltage Note 2	V _{GS} = 0V, I _S = 34.7A	V_{SD}			1.2	V

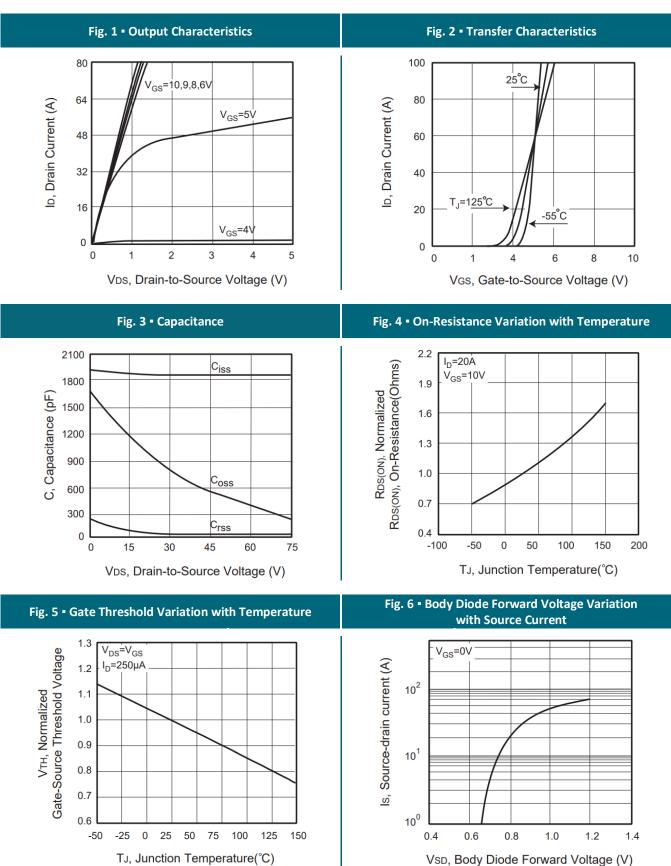
Notes

- 1: Repetitive Rating: Pulse width limited by maximum junction temperature
- 2: Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- 3: Guaranteed by design, not subject to production testing.
- 4: Limited only by maximum temperature allowed.
- 5: Pulse width limited by safe operating area.
- 6: $L = 0.1 \text{mH}, I_{AS} = 25 \text{A}, V_{DD} = 50 \text{V}, R_G = 25 \Omega, \text{ Starting } T_J = 25 ^{\circ}\text{C}.$



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REFERENCE DATA ▲ TYPICAL DEVICE PERFORMANCE



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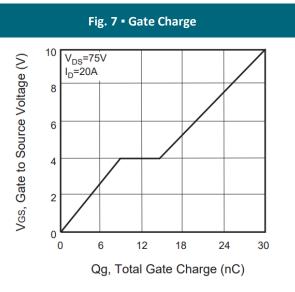
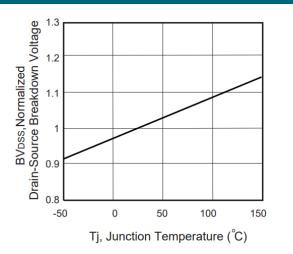


Fig. 9 - Breakdown Voltage Variation vs. Temperature



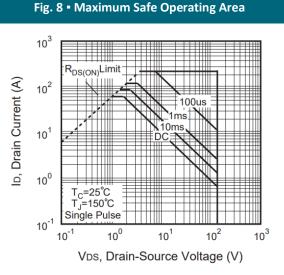


Fig. 11 • Switching Waveforms

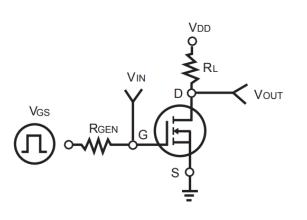
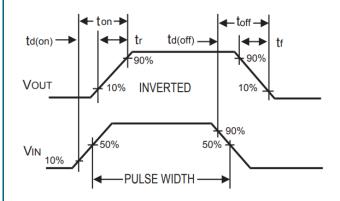


Fig. 10 • Switching Test Circuit



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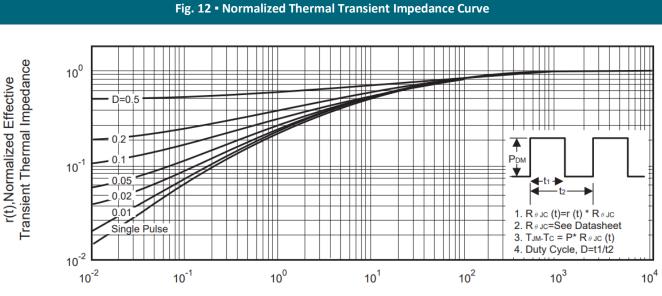
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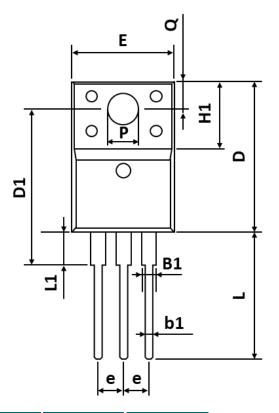


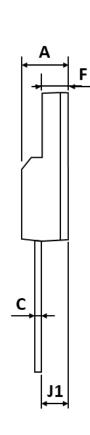
Square Wave Pulse Duration (msec)



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PACKAGE OUTLINE





Sym	Millimeters (Min.)	Millimeters (Typ.)	Millimeters (Max.)
А	4.500	-	5.000
B1	1.000	-	1.500
b1	0.700	-	0.950
С	0.420	-	0.700
D	15.670	-	16.070
D1	14.800	-	16.000
E	9.960	-	10.360
е	2.340	-	2.740
F	2.340	-	2.740
H1	6.480	-	6.900
J1	2.550	-	2.950
L	12.080	-	13.480
L1	2.230	-	3.650
Q	3.100	-	3.500
Р	2.980	-	3.380

ORDERING INFORMATION

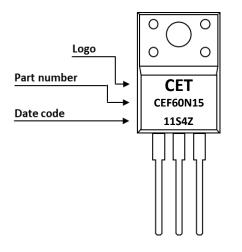
Part Number	Package	Packing	Tube Qty.	Inner Box Qty.	Outer Box Qty.
CEF60N15	TO-220F-3L	Tube	50pcs	1,000pcs	4,000pcs

SILICON (Si) POWER MOSFET A CEF60N15



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PART MARKING



DATE CODE

Example: 11S4Z



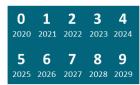
| Product Type Z: Pb-free G: Green Product

	Coding list for "Day"								
1	2	3	4	5	6	7	8	9	A
01	02	03	04	05	06	07	08	09	10
B	C	D	E	F	G	H	┃	J	K
11	12	13	14	15	16	17	18	19	20
L	M	N	0	P	Q	R	S	T	U
21	22	23	24	25	26	27	28	29	30
V 31									

Coding list for "Month"

1 Jan			5 May	
7	8	A	B	C
Jul	Aug	Oct	Nov	Dec

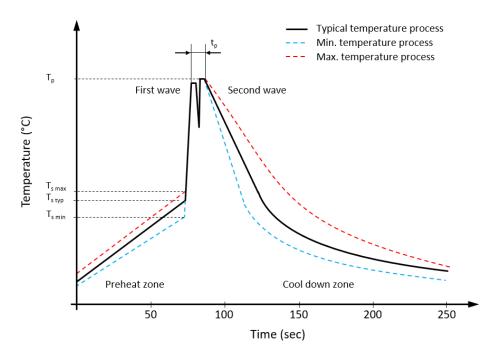
Coding list for "Year"







RECOMMENDED WAVE SOLDERING PROFILE ▲ THT PACKAGE



Classification wave soldering profile ▲ Refer to EN 61760-1: 2006

Profile Features		Value 🛦 Sn-Pb Assembly	Value 🔺 Pb-free Assembly
Preheat temperature min.	$T_{s min}$	100 °C	100 °C
Preheat temperature typical	T _{s typ}	120 °C	120 °C
Preheat temperature max.	$T_{s max}$	130 °C	130 °C
Preheat time t_s from $T_{s min}$ to $T_{s max}$	ts	70 seconds	70 seconds
Peak temperature	Tp	235 °C to 260 °C	245 °C to 260 °C
Time of actual peak temperature	t _p	Max. 10 seconds Max. 5 second each wave	Max. 10 seconds Max. 5 second each wave
Ramp-down date min.		~ 2 °C/second	~ 2 °C/second
Ramp-down rate typical		~ 3.5 °C/second	~ 3.5 °C/second
Ramp-down rate max.		~ 5 °C/second	~ 5 °C/second
Time 25°C to 25°C		4 minutes	4 minutes



REVISION TABLE

Revision	Date	Status	Notes
001	30/09/2022	Initial release	Initial publication

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