











CEM8958A

30V/-30V ▲ 20mΩ/36mΩ ▲ N&P Si MOSFET

SILICON Si MOSFET ▲ SMD type N- and P-channel enhancement mode UL94V-0 rated flame retardant epoxy SO8 package ▲ MSL 3

Super high dense cell density for extremely low R_{DS(ON)} High power and current handling capability

MAXIMUM RATINGS

Parameter (T _A = 25°C, unless otherwise noted)		N-Channel	P-Channel	
Drain-Source Voltage	V _{DS}	30V	-30V	
Gate-Source Voltage	V _{GS}	±20V	±20V	
Continuous Drain Current at T _A = 25°C	Ι _D	6.8A	-5.4A	
Continuous Drain Current at T _A = 70°C	I _D	5.4A	-4.3A	
Pulsed Drain Current Note 1	I _{DM}	27A	-21A	
Maximum Power Dissipation	P _D	2W		
Operating and Storage Temperature Range	T _J , T _{STG}	-55°C to +150°C		

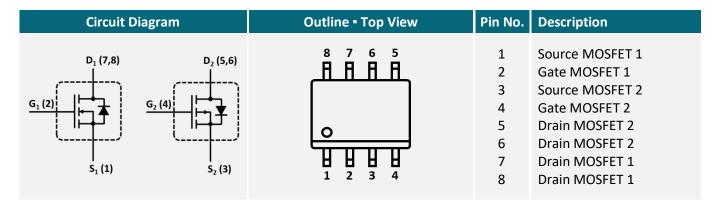
THERMAL CHARACTERISTICS

Parameter	Symbol	Limit
Thermal Resistance, Junction-to-Ambient Note 2	R _{TH_JA}	62.5°C/W
Thermal Resistance, Junction-to-Case	R _{TH_JC}	40°C/W

APPLICATIONS

Battery	DC/DC	High Side	Low Side	Power
Pack	Converter	Switches	Switches	Banks
+4-	<u>=/</u>			+

PIN DESCRIPTION





N-CHANNEL ELECTRICAL CHARACTERISTICS ▲ T_A = 25°C, unless otherwise noted

Item	Condition	Symbol	Min.	Тур.	Max.	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	BV _{DSS}	30			V
Zero Gate Voltage Drain Current	$V_{DS} = 30V, V_{GS} = 0V$	I _{DSS}			1	μΑ
Gate Body Leakage Current, Forward	$V_{GS} = 20V, V_{DS} = 0V$	I_{GSSF}			100	nA
Gate Body Leakage Current, Reverse	$V_{GS} = -20V$, $V_{DS} = 0V$	I_{GSSR}			-100	nA
On Characteristics Note 3						
Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$	$V_{GS(th)}$	1		2.5	V
Static Drain-Source On-Resistance	$V_{GS} = 10V, I_D = 6.8A$	R _{DS(ON)}		20	28	mΩ
Static Drain-Source On-Resistance	$V_{GS} = 4.5V$, $I_{D} = 6A$	R _{DS(ON)}		28	42	mΩ
Dynamic Characteristics Note 4						
Input Capacitance	$V_{DS} = 15V, V_{GS} = 0V, f = 1MHz$	C _{ISS}		345		pF
Output Capacitance	$V_{DS} = 15V, V_{GS} = 0V, f = 1MHz$	Coss		105		pF
Reverse Transfer Capacitance	$V_{DS} = 15V$, $V_{GS} = 0V$, $f = 1MHz$	C _{RSS}		70		pF
Switching Characteristics Note 4						
Turn-On Delay Time	V_{DD} = 15V, V_{GS} = 10V, I_{D} = 6.8A, $R_{G(ext)}$ = 2.7 Ω	t _{D(ON)}		8		ns
Turn-On Rise Time	V_{DD} = 15V, V_{GS} = 10V, I_{D} = 6.8A, $R_{G(ext)}$ = 2.7 Ω	t _R		5		ns
Turn-Off Delay Time	$V_{DD} = 15V$, $V_{GS} = 10V$, $I_D = 6.8A$, $R_{G(ext)} = 2.7\Omega$	t _{D(OFF)}		29		ns
Turn-Off Fall Time	V_{DD} = 15V, V_{GS} = 10V, I_{D} = 6.8A, $R_{G(ext)}$ = 2.7 Ω	$t_{\scriptscriptstyle{F}}$		8		ns
Total Gate Charge	$V_{DS} = 15V$, $V_{GS} = 10V$, $I_D = 6.8A$	Q_{G}		10.7		nC
Gate Source Charge	$V_{DS} = 15V$, $V_{GS} = 10V$, $I_D = 6.8A$	Q_{GS}		0.7		nC
Gate Drain Charge	$V_{DS} = 15V$, $V_{GS} = 10V$, $I_D = 6.8A$	Q_{GD}		3.3		nC
Drain-Source Diode Characteristics a	nd Maximum Ratings					
Drain-Source Diode Forward Current Note 2		Is			1.3	Α
Drain-Source Diode Forward Voltage Note 3	$V_{GS} = 0V$, $I_S = 1.3A$	V_{SD}			1.2	V

Notes

- 1: Repetitive Rating: Pulse width limited by maximum junction temperature
- 2: Surface Mounted on FR4 Board, t ≤ 10 sec
- 3: Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4: Guaranteed by design, not subject to production testing.



P-CHANNEL ELECTRICAL CHARACTERISTICS ▲ T_A = 25°C, unless otherwise noted

ltem	Condition	Symbol	Min.	Тур.	Max.	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	$V_{GS} = 0V$, $I_D = 250\mu A$	BV_{DSS}	-30			V
Zero Gate Voltage Drain Current	V_{DS} = -30V, V_{GS} = 0V	I _{DSS}			-1	μΑ
Gate Body Leakage Current, Forward	$V_{GS} = 20V, V_{DS} = 0V$	I _{GSSF}			100	nA
Gate Body Leakage Current, Reverse	$V_{GS} = -20V, V_{DS} = 0V$	I_{GSSR}			-100	nA
On Characteristics Note 3						
Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = -250 \mu A$	$V_{GS(th)}$	-1		-2.5	V
Static Drain-Source On-Resistance	$V_{GS} = -10V$, $I_D = -4.8A$	R _{DS(ON)}		36	45	mΩ
Static Drain-Source On-Resistance	$V_{GS} = -4.5V$, $I_D = -3.5A$	R _{DS(ON)}		60	80	mΩ
Dynamic Characteristics Note 4						
Input Capacitance	$V_{DS} = -15V$, $V_{GS} = 0V$, $f = 1MHz$	C _{ISS}		675		pF
Output Capacitance	$V_{DS} = -15V$, $V_{GS} = 0V$, $f = 1MHz$	Coss		120		pF
Reverse Transfer Capacitance	V_{DS} = -15V, V_{GS} = 0V, f = 1MHz	C _{RSS}		75		pF
Switching Characteristics Note 4						
Turn-On Delay Time	$V_{DD} = -15V$, $V_{GS} = -10V$, $I_D = -1A$, $R_{G(ext)} = 6\Omega$	t _{D(ON)}		12		ns
Turn-On Rise Time	V_{DD} = -15V, V_{GS} = -10V, I_D = -1A, $R_{G(ext)}$ = 6Ω	t_R		4		ns
Turn-Off Delay Time	V_{DD} = -15V, V_{GS} = -10V, I_D = -1A, $R_{G(ext)}$ = 6Ω	t _{D(OFF)}		54		ns
Turn-Off Fall Time	V_{DD} = -15V, V_{GS} = -10V, I_D = -1A, $R_{G(ext)}$ = 6Ω	t _F		9		ns
Total Gate Charge	V_{DD} = -15V, V_{GS} = -10V, I_D = -3.2A	Q_{G}		11.5		nC
Gate Source Charge	$V_{DD} = -15V$, $V_{GS} = -10V$, $I_D = -3.2A$	Q_{GS}		1.3		nC
Gate Drain Charge	V_{DD} = -15V, V_{GS} = -10V, I_D = -3.2A	Q_{GD}		2.3		nC
Drain-Source Diode Characteristics a	nd Maximum Ratings					
Drain-Source Diode Forward Current Note 2		Is			-1.3	Α
Drain-Source Diode Forward Voltage Note 3	$V_{GS} = 0V$, $I_S = -1.3A$	V_{SD}			-1.2	V

Notes

- 1: Repetitive Rating: Pulse width limited by maximum junction temperature
- 2: Surface Mounted on FR4 Board, t ≤ 10 sec
- 3: Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4: Guaranteed by design, not subject to production testing.



N-CHANNEL REFERENCE DATA A TYPICAL DEVICE PERFORMANCE



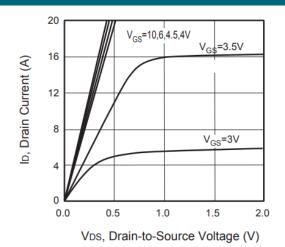


Fig. 2 • Transfer Characteristics

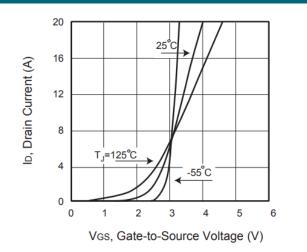


Fig. 3 • Capacitance

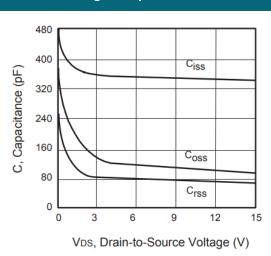


Fig. 4 • On-Resistance Variation with Temperature

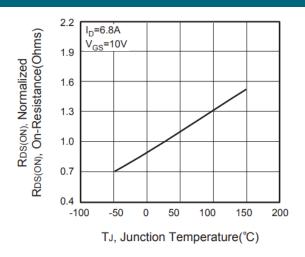


Fig. 5 • Gate Threshold Variation with Temperature

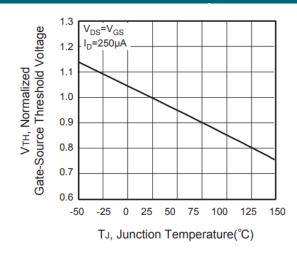
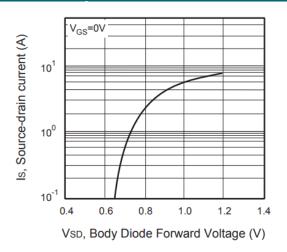


Fig. 6 • Body Diode Forward Voltage Variation with Source Current



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P-CHANNEL REFERENCE DATA A TYPICAL DEVICE PERFORMANCE



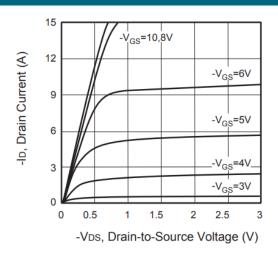


Fig. 8 • Transfer Characteristics

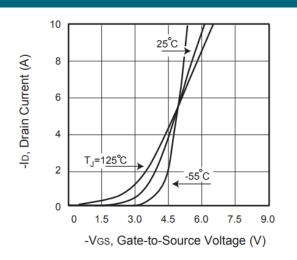


Fig. 9 • Capacitance

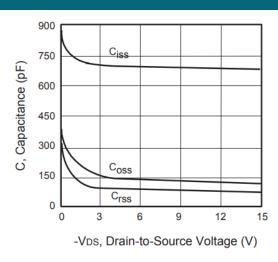


Fig. 10 • On-Resistance Variation with Temperature

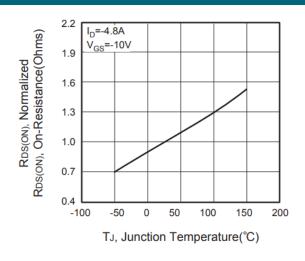


Fig. 11 • Gate Threshold Variation with Temperature

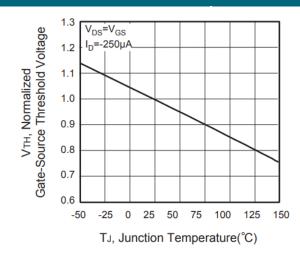
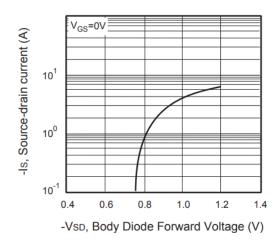


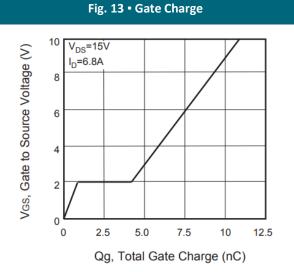
Fig. 12 • Body Diode Forward Voltage Variation with Source Current



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N-CHANNEL REFERENCE DATA A TYPICAL DEVICE PERFORMANCE



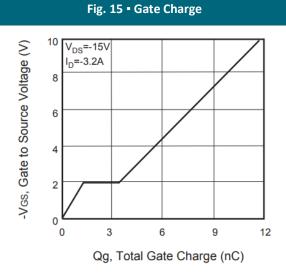
T_A=25°C T_J=150°C Single Pulse

10⁻²

VDs, Drain-Source Voltage (V)

Fig. 14 • Maximum Safe Operating Area

P-CHANNEL A REFERENCE DATA A TYPICAL DEVICE PERFORMANCE



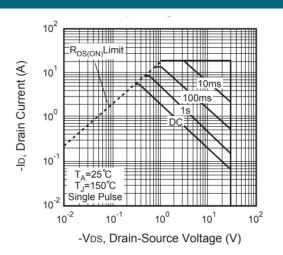


Fig. 16 • Maximum Safe Operating Area



REFERENCE DATA A TYPICAL DEVICE PERFORMANCE

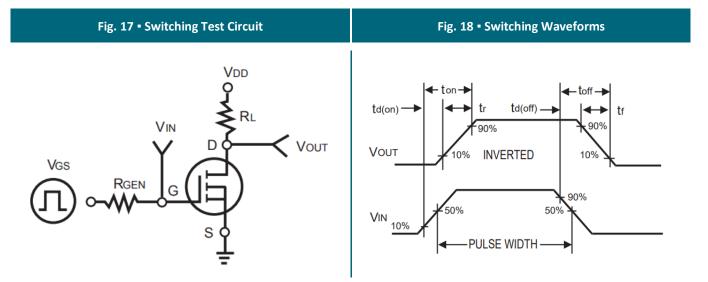
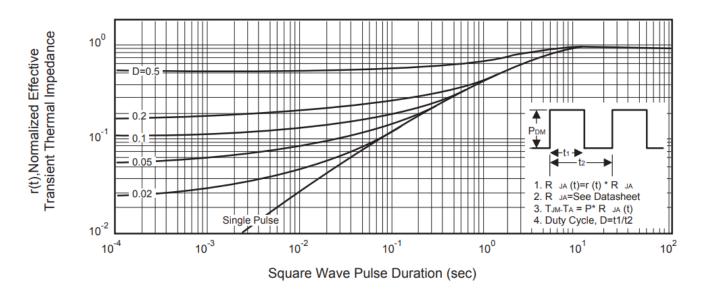
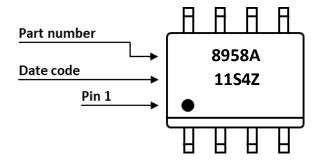


Fig. 19 • Normalized Thermal Transient Impedance Curve



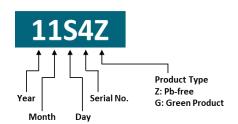


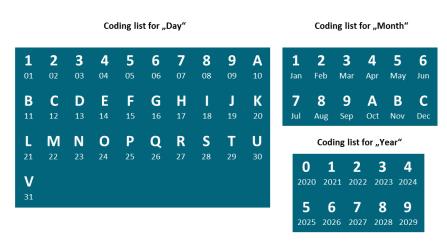
PART MARKING



DATE CODE

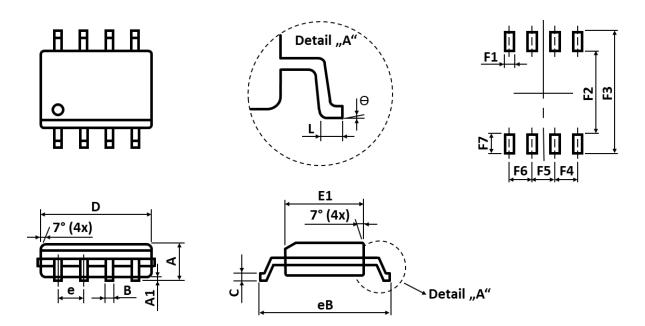
Example: 11S4Z







PACKAGE OUTLINE AND RECOMMENDED PAD LAYOUT



Sym	Millimeters (Min.)	Millimeters (Typ.)	Millimeters (Max.)
Α	1.350	-	1.750
A1	0.100	-	0.250
В	0.310	-	0.510
С	0.170	-	0.250
D	4.690	-	5.000

Sym	Millimeters (Min.)	Millimeters (Typ.)	Millimeters (Max.)
E1	3.700		4.060
еВ	5.800		6.200
е		1.270	
L	0.400		0.950
Θ	0°	-	8°

Sym	Millimeters (Min.)	Millimeters (Typ.)	Millimeters (Max.)
F1	_	0.500	_
F2	-	4.250	-
F3	-	6.250	-
F4	-	1.270	-

-
-
-

Notes: 1. The suggested land pattern dimensions have been provided for reference only.

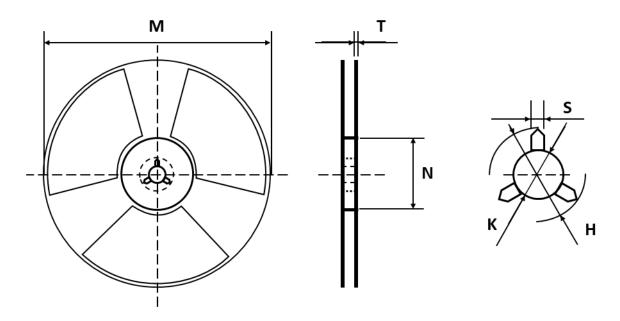
2. For further information, please reference document IPC-7351A.

ORDERING INFORMATION

Part Number	Package	Packing	Reel Qty.	Inner Box Qty.	Outer Box Qty.
CEM8958A	SO8	13" Reel	2,500pcs	5,000pcs	40,000pcs

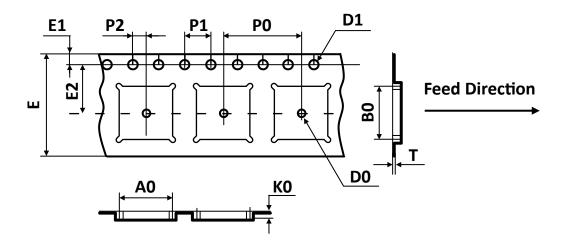


REEL DIMENSIONS ▲ All dimensions in mm



Tape Size	Reel Size	M	N	T	H	K	S
12mm	Ø330	Ø330.00	Ø100.00	2.20	20.00	13.20	3.00
	<i>y</i> 550	±2.00	±0.50	±0.20	±1.00	±0.20	±1.00

TAPE DIMENSIONS ▲ All dimensions in mm

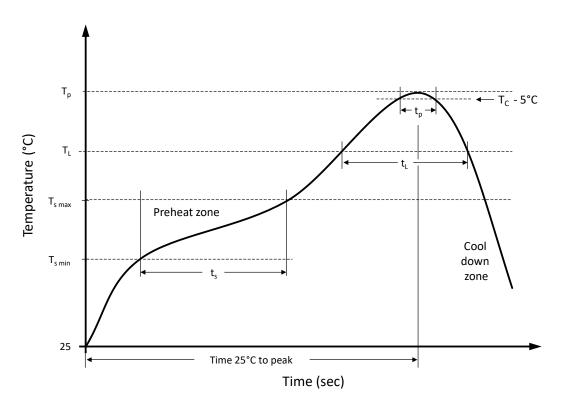


Package	A0	В0	КО	D0	D1	E	E1	E2	Р0	P1	P2	T
SO8	6.50	5.30	2.05	1.50	1.50	12.00	1.75	5.50	8.00	4.00	2.00	0.25
308	±0.10	±0.10	±0.15	±0.10	±0.10	±0.10	±0.10	±0.10	±0.10	±0.10	±0.05	±0.02

Note: All dimensions meet EIA-481-D requirements.



RECOMMENDED REFLOW SOLDERING PROFILE



Recommended reflow soldering conditions ▲ **Refer to JEDEC J-STD-020E**

Profile Features		Sn-Pb Eutetic Assembly	Pb-Free Assembly
Preheat temperature min.	$T_{s min}$	100 °C	150 °C
Preheat temperature max.	T _{s max}	150 °C	200 °C
Preheat time t _s from T _{s min} to T _{s max}	ts	120 seconds	120 seconds
Ramp-up rate (T₁ to Tp)		max. 3 °C/second	max. 3 °C/second
Liquidous temperature	T_L	183 °C	217 °C
Time t _L maintained above T _L	t _L	150 seconds max.	150 seconds max.
Peak package body temperature	Tp	235°C	260°C
Timeframe of within 5°C below and up to max actual peak body temperature	t _p	20 seconds max.	30 seconds max.
Ramp-down rate (T _L to T _p)		max. 6 °C/second	max. 6 °C/second
Time 25°C to peak temperature		max. 6 minutes	max. 8 minutes



REVISION TABLE

Revision	Date	Status	Notes
001	30/09/2022	Initial release	Initial publication

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