SILICON (Si) POWER MOSFET A CEP13N65S



CEP13N65S

650V ▲ 270mΩ ▲ 13.8A ▲ Si MOSFET

SILICON Si MOSFET ▲ THT type N-channel enhancement mode UL94V-0 rated flame retardant epoxy TO220-3L package Super high dense cell density for extremely low R_{DS(ON)} High power and current handling capability

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MAXIMUM RATINGS

Parameter (T _c = 25°C, unless otherwise noted)		Characteristics
Drain-Source Voltage	V _{DS}	650V
Gate-Source Voltage	V _{GS}	±30V
Continuous Drain Current at T _c = 25°C	Ι _D	13.8A
Continuous Drain Current at T _c = 100°C	Ι _D	6.2A
Pulsed Drain Current Note 1	IDM Note 5	55.2A
Maximum Power Dissipation at T _c = 25°C	PD	160W
Power Dissipation Derating above 25°C	ΔP _D	1.28W/°C
Single Pulsed Avalanche Energy Note 6	E _{AS}	306mJ
Single Pulsed Avalanche Current Note 6	I _{AS}	3.5A
Operating and Storage Temperature Range	T _J , T _{STG}	-55°C to +150°C

THERMAL CHARACTERISTICS

Parameter	Symbol	Limit
Thermal Resistance, Junction-to-Case	R _{TH_JC}	0.78°C/W
Thermal Resistance, Junction-to-Ambient	R _{th_ja}	62.5°C/W

APPLICATIONS



PIN DESCRIPTION

Circuit Diagram	Outline - Front View	Pin No.	Description
G (1)		1	Gate
G (1)		2	Drain
S (3)		3	Source

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ELECTRICAL CHARACTERISTICS A T_c = 25°C, unless otherwise noted

Item	Condition	Symbol	Min.	Тур.	Max.	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	BV _{DSS}	650			V
Zero Gate Voltage Drain Current	$V_{DS} = 650V, V_{GS} = 0V$	I _{DSS}			1	μA
Gate Body Leakage Current, Forward	$V_{GS} = 30V, V_{DS} = 0V$	I _{GSSF}			100	nA
Gate Body Leakage Current, Reverse	$V_{GS} = -30V, V_{DS} = 0V$	I _{GSSR}			-100	nA
On Characteristics Note 2						
Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 250 \mu A$	V _{GS(th)}	2.5		4.5	V
Static Drain-Source On-Resistance	$V_{GS} = 10V, I_D = 6A$	R _{DS(ON)}		270	320	mΩ
Dynamic Characteristics Note 3						
Input Capacitance	V_{DS} = 150V, V_{GS} = 0V, f = 1MHz	CISS		910		рF
Output Capacitance	V_{DS} = 150V, V_{GS} = 0V, f = 1MHz	Coss		60		рF
Reverse Transfer Capacitance	V_{DS} = 150V, V_{GS} = 0V, f = 1MHz	C _{RSS}		15		рF
Switching Characteristics Note 3						
Turn-On Delay Time	V_{DD} = 400V, V_{GS} = 10V, I_{D} = 6A, $R_{G(ext)}$ = 10 Ω	t _{D(ON)}		30		ns
Turn-On Rise Time	V_{DD} = 400V, V_{GS} = 10V, I_{D} = 6A, $R_{G(ext)}$ = 10 Ω	t _R		13		ns
Turn-Off Delay Time	V_{DD} = 400V, V_{GS} = 10V, I_{D} = 6A, $R_{\text{G(ext)}}$ = 10 Ω	t _{D(OFF)}		65		ns
Turn-Off Fall Time	V_{DD} = 400V, V_{GS} = 10V, I_{D} = 6A, $R_{\text{G(ext)}}$ = 10 Ω	t _F		11		ns
Total Gate Charge	V_{DS} = 400V, V_{GS} = 10V, I_{D} = 1A	Q_{G}		25		nC
Gate Source Charge	V_{DS} = 400V, V_{GS} = 10V, I_{D} = 1A	Q _{GS}		4		nC
Gate Drain Charge	V_{DS} = 400V, V_{GS} = 10V, I_D = 1A	\mathbf{Q}_{GD}		10		nC
Drain-Source Diode Characteristics a	nd Maximum Ratings					
Drain-Source Diode Forward Current		I _S			15	A
Drain-Source Diode Forward Voltage ^{Note 2}	$V_{GS} = 0V$, $I_{S} = 6A$	V_{SD}			1.2	V
Reverse Recovery Time	I _D = 6A, di/dt = 100A/μs	t _{RR}		240		ns
Reverse Recovery Charge	I _D = 6A, di/dt = 100A/μs	Q _{RR}		2.35		μC
Peak Reverse Recovery Current	I _D = 6A, di/dt = 100A/μs	I _{RR}		16.8		А

Notes

1: Repetitive Rating: Pulse width limited by maximum junction temperature

2: Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

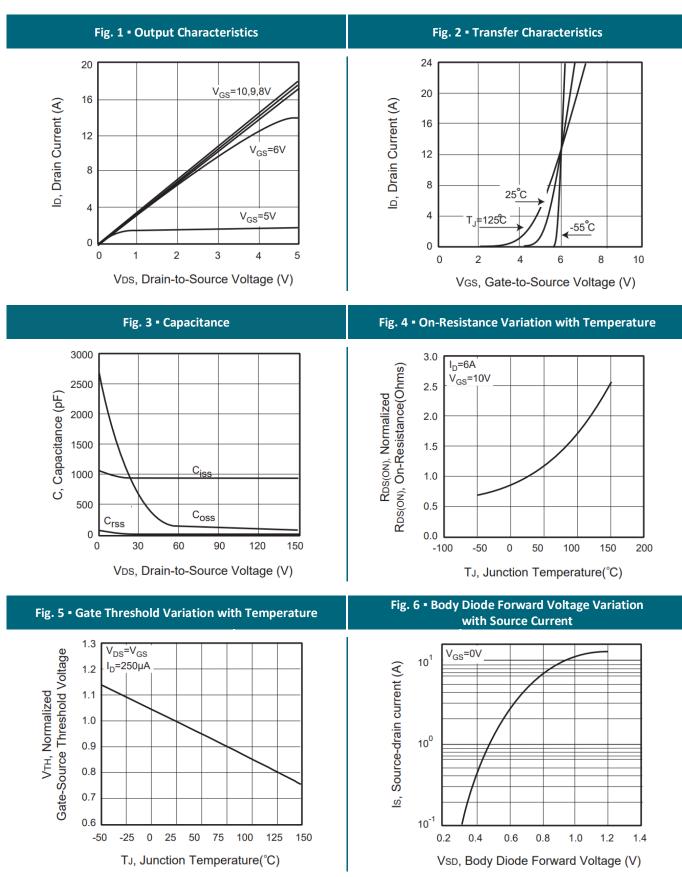
- 3: Guaranteed by design, not subject to production testing.
- 4: Limited only by maximum temperature allowed.
- 5: Pulse width limited by safe operating area.
- 6: L = 50mH, I_{AS} = 3.5A, V_{DD} = 50V, R_G = 25Ω, Starting T_J = 25°C



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REFERENCE DATA ▲ TYPICAL DEVICE PERFORMANCE



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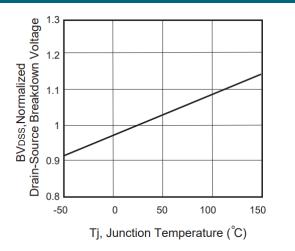


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REFERENCE DATA A TYPICAL DEVICE PERFORMANCE

Fig. 7 • Gate Charge 10 V_{DS}=400V I_D=1A VGS, Gate to Source Voltage (V) 8 6 4 2 0 0 5 10 15 20 25 Qg, Total Gate Charge (nC)

Fig. 9 - Breakdown Voltage Variation vs. Temperature



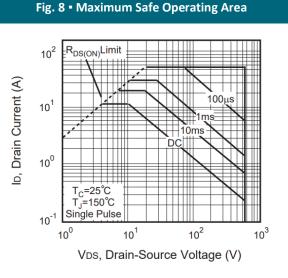
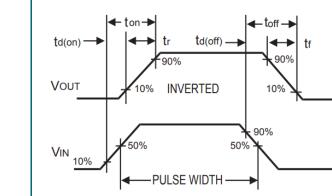


Fig. 11 • Switching Waveforms



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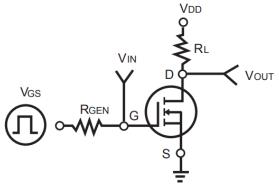


Fig. 10 • Switching Test Circuit

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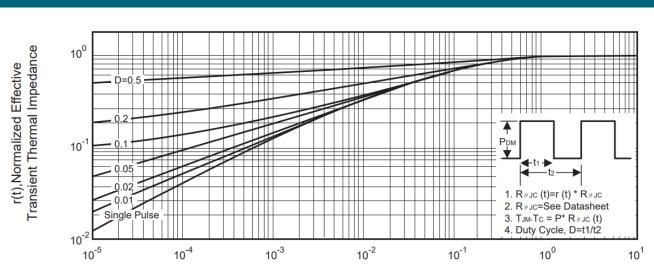


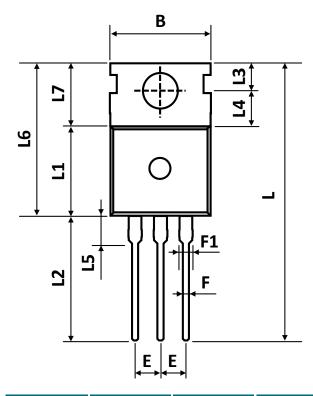
Fig. 12 • Normalized Thermal Transient Impedance Curve

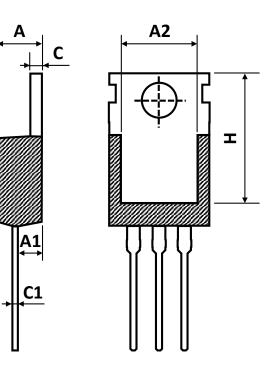
Square Wave Pulse Duration (sec)



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PACKAGE OUTLINE





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Sym	Millimeters (Min.)	Millimeters (Typ.)	Millimeters (Max.)		
А	4.43	4.53	4.63		
A1	2.30	2.40	2.50		
A2	7.70	7.90	8.10		
В	9.80	10.00	10.20		
С	1.25	1.30	1.40		
C1	0.45	0.50	0.60		
D	3.45	3.60	3.70		
E	2.45	2.54	2.60		
F	0.70	0.80	0.95		
F1	1.15	1.33	1.50		
L	26.80	28.80	30.80		
L1	9.20	9.30	9.40		
L2	12.80	13.10	13.40		
L3	2.70	2.80	2.90		
L4	3.50	3.70	3.80		
L5	2.60	2.90	3.20		
L6	15.40	15.80	16.20		
L7	6.20	6.50	6.80		
Н	12.95	13.25	13.55		

ORDERING INFORMATION

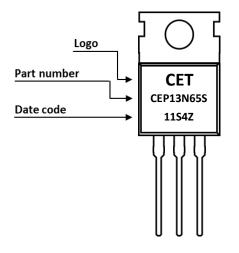
Part Number	Package	Packing	Tube Qty.	Inner Box Qty.	Outer Box Qty.
CEP13N65S	TO-220-3L	Tube	50pcs	1,000pcs	4,000pcs

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PART MARKING



DATE CODE

Example: 11S4Z



| Product Type Z: Pb-free G: Green Product

	Coding list for "Day"								
1	2	3	4	5	6	7	8	9	A
01	02	03	04	05	06	07	08	09	10
B	C	D	E	F	G	H	 	J	K
11	12	13	14	15	16	17	18	19	20
L	M	N	0	P	Q	R	S	T	U
21	22	23	24	25	26	27	28	29	30
V 31									

Coding list for "Month"

1 Jan	2 Feb			5 May	
7	8	9	A	B	C
Jul	Aug	Sep	Oct	Nov	Dec

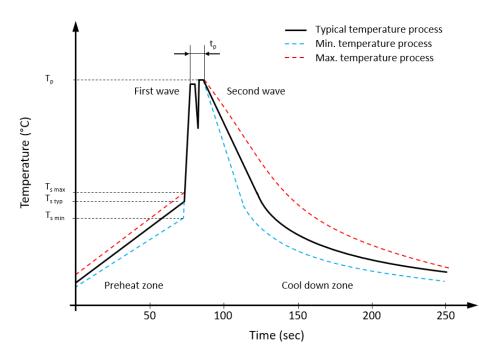
Coding list for "Year"







RECOMMENDED WAVE SOLDERING PROFILE ▲ THT PACKAGE



Classification wave soldering profile ▲ Refer to EN 61760-1: 2006

Profile Features		Value 🛦 Sn-Pb Assembly	Value 🔺 Pb-free Assembly
Preheat temperature min.	$T_{s min}$	100 °C	100 °C
Preheat temperature typical	T _{s typ}	120 °C	120 °C
Preheat temperature max.	$T_{s max}$	130 °C	130 °C
Preheat time t_s from $T_{s min}$ to $T_{s max}$	ts	70 seconds	70 seconds
Peak temperature	Tp	235 °C to 260 °C	245 °C to 260 °C
Time of actual peak temperature	t _p	Max. 10 seconds Max. 5 second each wave	Max. 10 seconds Max. 5 second each wave
Ramp-down date min.		~ 2 °C/second	~ 2 °C/second
Ramp-down rate typical		~ 3.5 °C/second	~ 3.5 °C/second
Ramp-down rate max.		~ 5 °C/second	~ 5 °C/second
Time 25°C to 25°C		4 minutes	4 minutes

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REVISION TABLE

Revision	Date	Status	Notes
001	30/09/2022	Initial release	Initial publication

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