









# **CEP3205**

#### 55V Δ 6.5mΩ Δ 108.5A Δ Si MOSFET

SILICON Si MOSFET ▲ THT type
N-channel enhancement mode
UL94V-0 rated flame retardant epoxy
TO220-3L package

Super high dense cell density for extremely low R<sub>DS(ON)</sub> **High power and current handling capability** 

### **MAXIMUM RATINGS**

Parameter (T <sub>C</sub> = 25°C, unless otherwise noted)		Characteristics
Drain-Source Voltage	V <sub>DS</sub>	55V
Gate-Source Voltage	V <sub>GS</sub>	±20V
Continuous Drain Current at T <sub>C</sub> = 25°C	I <sub>D</sub>	108.5A
Continuous Drain Current at T <sub>C</sub> = 100°C	I <sub>D</sub>	76.7A
Pulsed Drain Current Note 1	I <sub>DM</sub>	434A
Maximum Power Dissipation at T <sub>C</sub> = 25°C	P <sub>D</sub>	200W
Power Dissipation Derating above 25°C	$\Delta P_D$	1.3W/°C
Repetitive Avalanche Energy	E <sub>AR</sub>	1.38mJ
Single Pulsed Avalanche Energy Note 4	E <sub>AS</sub>	319mJ
Single Pulsed Avalanche Current Note 4	I <sub>AS</sub>	68A
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55°C to +175°C

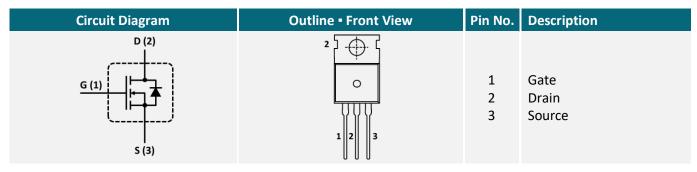
### THERMAL CHARACTERISTICS

Parameter	Symbol	Limit
Thermal Resistance, Junction-to-Case	R <sub>TH_JC</sub>	0.75°C/W
Thermal Resistance, Junction-to-Ambient	R <sub>TH JA</sub>	62.5°C/W

#### **APPLICATIONS**

Battery Management Systems	DC/DC	High Side	Industrial	Low Side
	Converter	Switches	Control	Switches
+4-				

#### **PIN DESCRIPTION**





## **ELECTRICAL CHARACTERISTICS** ▲ T<sub>C</sub> = 25°C, unless otherwise noted

Item	Condition	Symbol	Min.	Тур.	Max.	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	$V_{GS} = 0V$ , $I_D = 250\mu A$	$BV_DSS$	55			V
Zero Gate Voltage Drain Current	$V_{DS} = 55V, V_{GS} = 0V$	I <sub>DSS</sub>			1	μΑ
Gate Body Leakage Current, Forward	$V_{GS} = 20V, V_{DS} = 0V$	$I_{GSSF}$			100	nA
Gate Body Leakage Current, Reverse	$V_{GS} = -20V, V_{DS} = 0V$	I <sub>GSSR</sub>			-100	nA
On Characteristics Note 2						
Gate Threshold Voltage	$V_{GS} = V_{DS}$ , $I_D = 250 \mu A$	$V_{GS(th)}$	2		4	V
Static Drain-Source On-Resistance	$V_{GS} = 10V, I_D = 62A$	R <sub>DS(ON)</sub>		6.5	8.5	mΩ
Dynamic Characteristics Note 3						
Input Capacitance	$V_{DS} = 25V$ , $V_{GS} = 0V$ , $f = 1MHz$	C <sub>ISS</sub>		5040		pF
Output Capacitance	$V_{DS} = 25V$ , $V_{GS} = 0V$ , $f = 1MHz$	Coss		1115		рF
Reverse Transfer Capacitance	$V_{DS} = 25V$ , $V_{GS} = 0V$ , $f = 1MHz$	$C_{RSS}$		35		pF
Switching Characteristics Note 3						
Turn-On Delay Time	$V_{DD}$ = 28V, $V_{GS}$ = 10V, $I_D$ = 62A, $R_{G(ext)}$ = 4.5 $\Omega$	t <sub>D(ON)</sub>		27	54	ns
Turn-On Rise Time	$V_{DD}$ = 28V, $V_{GS}$ = 10V, $I_D$ = 62A, $R_{G(ext)}$ = 4.5 $\Omega$	t <sub>R</sub>		14	28	ns
Turn-Off Delay Time	$V_{DD}$ = 28V, $V_{GS}$ = 10V, $I_D$ = 62A, $R_{G(ext)}$ = 4.5 $\Omega$	t <sub>D(OFF)</sub>		68	136	ns
Turn-Off Fall Time	$V_{DD}$ = 28V, $V_{GS}$ = 10V, $I_D$ = 62A, $R_{G(ext)}$ = 4.5 $\Omega$	t <sub>F</sub>		19	38	ns
Total Gate Charge	$V_{DS} = 44V$ , $V_{GS} = 10V$ , $I_{D} = 62A$	$Q_{G}$		102.3	136	nC
Gate Source Charge	$V_{DS} = 44V$ , $V_{GS} = 10V$ , $I_{D} = 62A$	$Q_{GS}$		23.1		nC
Gate Drain Charge	$V_{DS} = 44V$ , $V_{GS} = 10V$ , $I_D = 62A$	$Q_{GD}$		23.1		nC
Drain-Source Diode Characteristics and Maximum Ratings						
Drain-Source Diode Forward Current		I <sub>S</sub>			108.5	Α
Drain-Source Diode Forward Voltage Note 2	V <sub>GS</sub> = 0V, I <sub>S</sub> = 62A	$V_{\text{SD}}$			1.3	V

#### Notes

- 1: Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2: Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 3: Guaranteed by design, not subject to production testing.
- 4: L = 138μH,  $I_{AS}$  = 68A,  $V_{DD}$  = 25V,  $R_G$  = 25Ω, Starting  $T_J$  = 25°C.



#### REFERENCE DATA A TYPICAL DEVICE PERFORMANCE



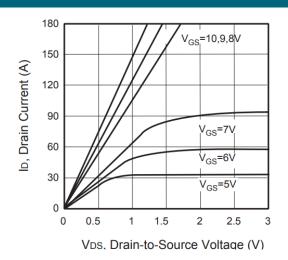


Fig. 2 • Transfer Characteristics

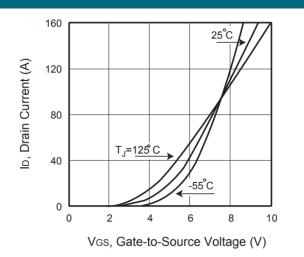


Fig. 3 • Capacitance

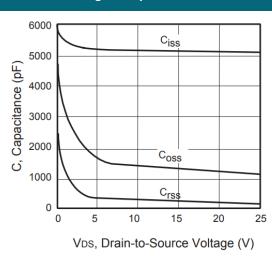


Fig. 4 • On-Resistance Variation with Temperature

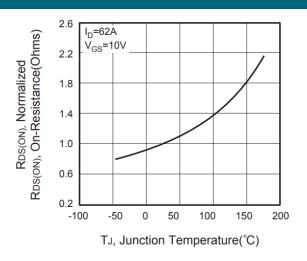


Fig. 5 • Gate Threshold Variation with Temperature

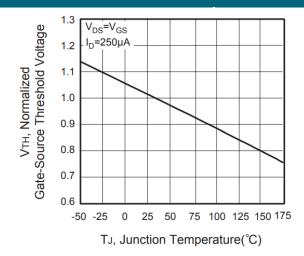
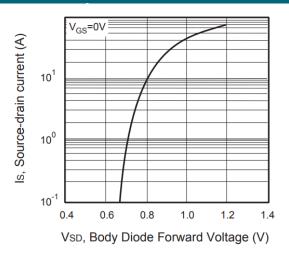


Fig. 6 • Body Diode Forward Voltage Variation with Source Current





#### REFERENCE DATA A TYPICAL DEVICE PERFORMANCE

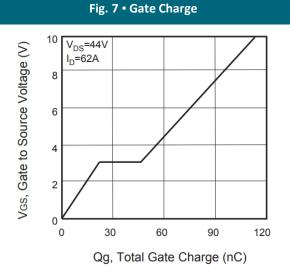


Fig. 8 • Maximum Safe Operating Area

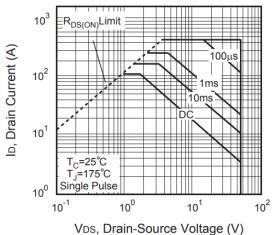
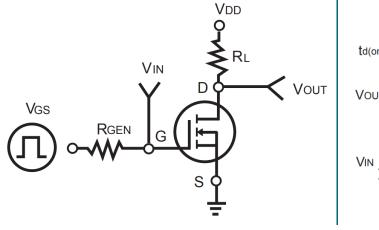


Fig. 9 • Switching Test Circuit

Fig. 10 • Switching Waveforms



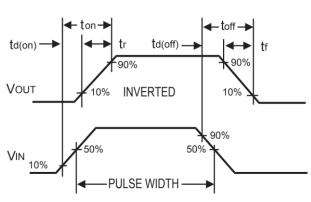
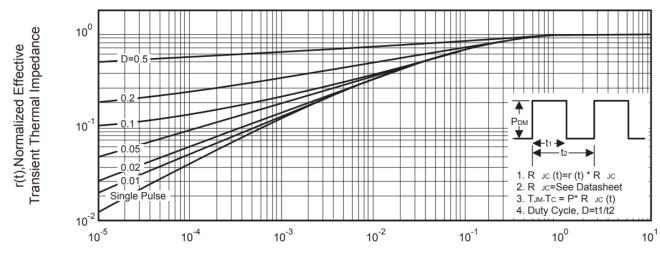


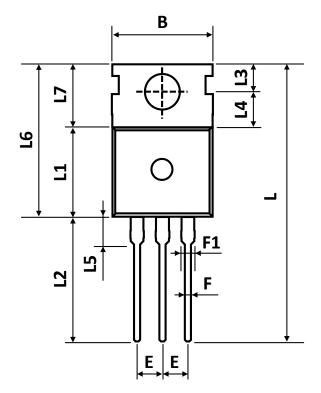
Fig. 11 - Normalized Thermal Transient Impedance Curve

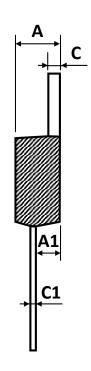


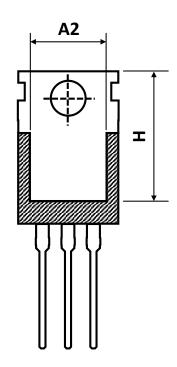
Square Wave Pulse Duration (sec)



## **PACKAGE OUTLINE**







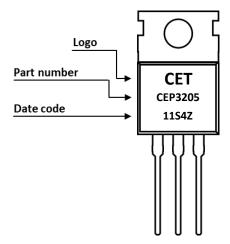
Sym	Millimeters (Min.)	Millimeters (Typ.)	Millimeters (Max.)
Α	4.43	4.53	4.63
A1	2.30	2.40	2.50
A2	7.70	7.90	8.10
В	9.80	10.00	10.20
С	1.25	1.30	1.40
C1	0.45	0.50	0.60
D	3.45	3.60	3.70
E	2.45	2.54	2.60
F	0.70	0.80	0.95
F1	1.15	1.33	1.50
L	26.80	28.80	30.80
L1	9.20	9.30	9.40
L2	12.80	13.10	13.40
L3	2.70	2.80	2.90
L4	3.50	3.70	3.80
L5	2.60	2.90	3.20
L6	15.40	15.80	16.20
L7	6.20	6.50	6.80
Н	12.95	13.25	13.55

## **ORDERING INFORMATION**

Part Number	Package	Packing	Tube Qty.	Inner Box Qty.	Outer Box Qty.
CEP3205	TO-220-3L	Tube	50pcs	1,000pcs	4,000pcs



#### **PART MARKING**



#### **DATE CODE**

Example: 11S4Z



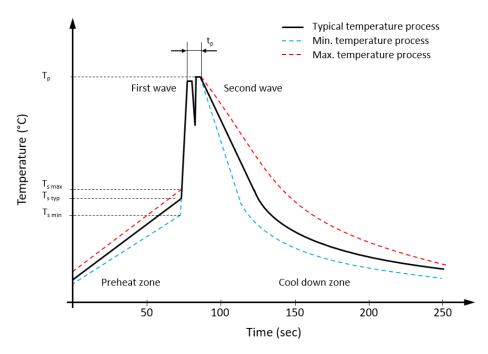


Coding list for "Day"





### RECOMMENDED WAVE SOLDERING PROFILE A THT PACKAGE



## Classification wave soldering profile ▲ Refer to EN 61760-1: 2006

Profile Features		Value ▲ Sn-Pb Assembly	Value ▲ Pb-free Assembly
Preheat temperature min.	$T_{s  min}$	100 °C	100 °C
Preheat temperature typical	T <sub>s typ</sub>	120 °C	120 °C
Preheat temperature max.	T <sub>s max</sub>	130 °C	130 °C
Preheat time $t_s$ from $T_{smin}$ to $T_{smax}$	ts	70 seconds	70 seconds
Peak temperature	Tp	235 °C to 260 °C	245 °C to 260 °C
Time of actual peak temperature	t <sub>p</sub>	Max. 10 seconds Max. 5 second each wave	Max. 10 seconds Max. 5 second each wave
Ramp-down date min.		~ 2 °C/second	~ 2 °C/second
Ramp-down rate typical		~ 3.5 °C/second	~ 3.5 °C/second
Ramp-down rate max.		~ 5 °C/second	~ 5 °C/second
Time 25°C to 25°C		4 minutes	4 minutes



#### **REVISION TABLE**

Revision	Date	Status	Notes
001	30/09/2022	Initial release	Initial publication

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