SILICON (Si) POWER MOSFET ▲ CEU6040SL



CEU6040SL

65V ▲ 4mΩ ▲ 89A ▲ Si MOSFET

SILICON Si MOSFET ▲ SMD type N-channel enhancement mode UL94V-0 rated flame retardant epoxy TO252 (DPAK) package ▲ MSL 3 Super high dense cell density for extremely low R_{DS(ON)} High power and current handling capability

MAXIMUM RATINGS

Parameter (T_c = 25°C, unless otherwise noted)	Characteristics	
Drain-Source Voltage	V _{DS}	65V
Gate-Source Voltage	V _{GS}	±20V
Continuous Drain Current at T _c = 25°C	I _D	89A
Continuous Drain Current at T _c = 100°C	I _D	63A
Pulsed Drain Current Note 1	I _{DM} Note4	356A
Maximum Power Dissipation at T _c = 25°C	PD	68W
Power Dissipation Derating above 25°C	ΔΡ _D	0.45W/°C
Single Pulsed Avalanche Energy Note 5	E _{AS}	250mJ
Single Pulsed Avalanche Current Note 5	I _{AS}	10A
Operating and Storage Temperature Range	T _J , T _{STG}	-55°C to +175°C

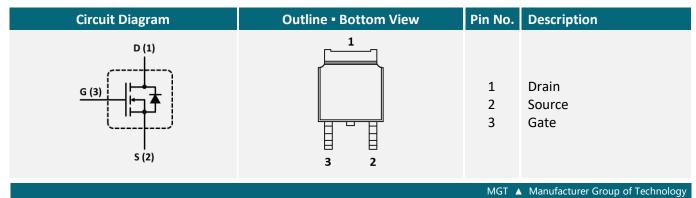
THERMAL CHARACTERISTICS

Parameter	Symbol	Limit
Thermal Resistance, Junction-to-Case	R _{TH_JC}	2.2°C/W
Thermal Resistance, Junction-to-Ambient Note 2	R _{th_ja}	50°C/W

APPLICATIONS



PIN DESCRIPTION



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ELECTRICAL CHARACTERISTICS A T_c = 25°C, unless otherwise noted

Item	Condition	Symbol	Min.	Тур.	Max.	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	BV _{DSS}	65			V
Zero Gate Voltage Drain Current	$V_{DS} = 65V, V_{GS} = 0V$	I _{DSS}			1	μΑ
Gate Body Leakage Current, Forward	$V_{GS} = 20V$, $V_{DS} = 0V$	I _{GSSF}			100	nA
Gate Body Leakage Current, Reverse	V_{GS} = -20V, V_{DS} = 0V	I _{GSSR}			-100	nA
On Characteristics Note 2						
Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 250 \mu A$	V _{GS(th)}	1		3	V
Static Drain-Source On-Resistance	$V_{GS} = 10V, I_{D} = 20A$	R _{DS(ON)}		4	4.8	mΩ
Static Drain-Source On-Resistance	V_{GS} = 4.5V, I _D = 15A	R _{DS(ON)}		6	7.8	mΩ
Dynamic Characteristics Note 3						
Input Capacitance	V_{DS} = 30V, V_{GS} = 0V, f = 1MHz	CISS		1790		рF
Output Capacitance	$V_{DS} = 30V, V_{GS} = 0V, f = 1MHz$	Coss		725		рF
Reverse Transfer Capacitance	V_{DS} = 30V, V_{GS} = 0V, f = 1MHz	C _{RSS}		15		pF
Switching Characteristics Note 3						
Turn-On Delay Time	V_{DD} = 30V, V_{GS} = 10V, I_D = 20A, $R_{G(ext)}$ = 25 Ω	t _{D(ON)}		22		ns
Turn-On Rise Time	V_{DD} = 30V, V_{GS} = 10V, I_{D} = 20A, $R_{\text{G(ext)}}$ = 25 Ω	t _R		28		ns
Turn-Off Delay Time	V_{DD} = 30V, V_{GS} = 10V, I_{D} = 20A, $R_{\text{G(ext)}}$ = 25 Ω	t _{D(OFF)}		143		ns
Turn-Off Fall Time	V_{DD} = 30V, V_{GS} = 10V, I_{D} = 20A, $R_{\text{G(ext)}}$ = 25 Ω	t _F		90		ns
Total Gate Charge	$V_{DS} = 30V, V_{GS} = 4.5V, I_D = 20A$	Q _G		26		nC
Gate Source Charge	$V_{DS} = 30V, V_{GS} = 4.5V, I_D = 20A$	Q _{GS}		4		nC
Gate Drain Charge	V_{DS} = 30V, V_{GS} = 4.5V, I_{D} = 20A	\mathbf{Q}_{GD}		15		nC
Drain-Source Diode Characteristics a	nd Maximum Ratings					
Drain-Source Diode Forward Current		I _S			56	А
Drain-Source Diode Forward Voltage Note 2	V _{GS} = 0V, I _S = 20A	V_{SD}			1.2	V

Notes

1: Repetitive Rating: Pulse width limited by maximum junction temperature

2: Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

3: Guaranteed by design, not subject to production testing.

4: Pulse width limited by safe operating area.

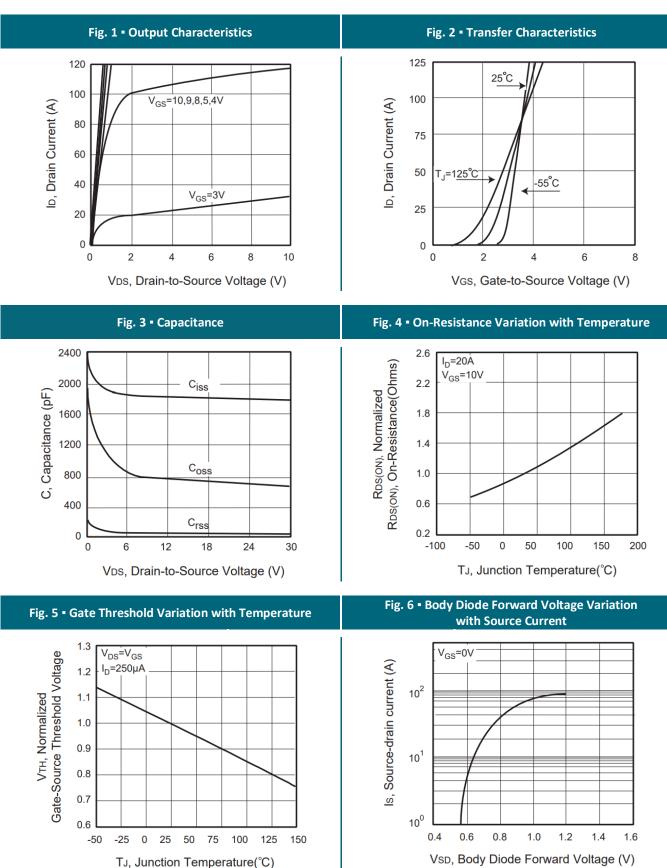
5: L = 5mH, $I_{AS} = 10A$, $V_{DD} = 24V$, $R_G = 25\Omega$, Starting $T_J = 25^{\circ}C$



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REFERENCE DATA ▲ TYPICAL DEVICE PERFORMANCE



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REFERENCE DATA A TYPICAL DEVICE PERFORMANCE

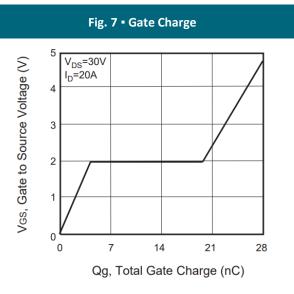
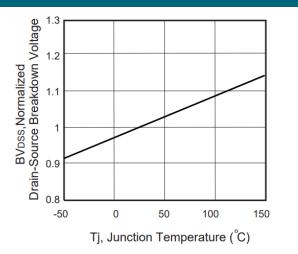


Fig. 9 - Breakdown Voltage Variation vs. Temperature



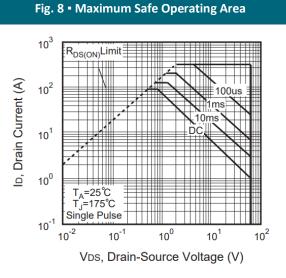
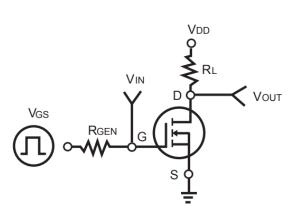
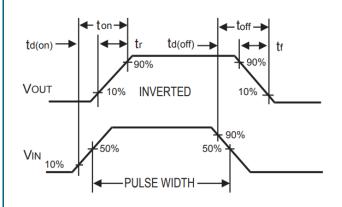


Fig. 10 • Switching Test Circuit







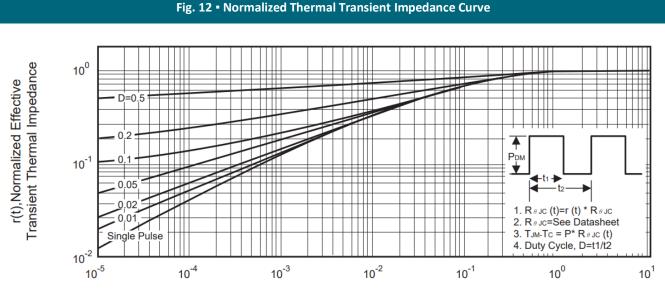
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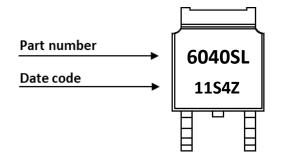


REFERENCE DATA ▲ TYPICAL DEVICE PERFORMANCE



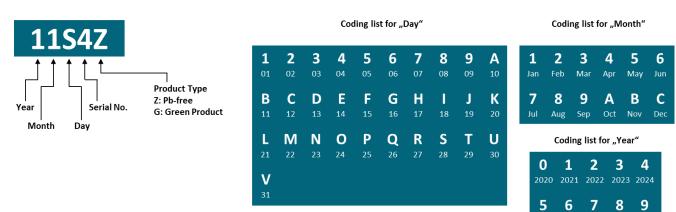
Square Wave Pulse Duration (sec)

PART MARKING



DATE CODE

Example: 11S4Z

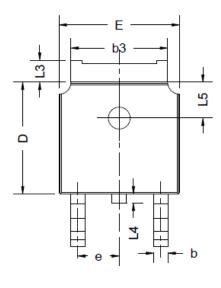


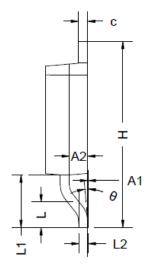
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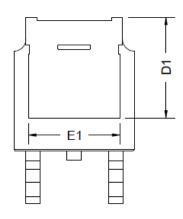
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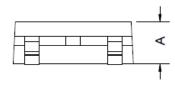


PACKAGE OUTLINE









Sym	Millimeters (Min.)	Millimeters (Typ.)	Millimeters (Max.)	Sym	Millimeters (Min.)	Millimeters (Typ.)	Millimeters (Max.)
А	2.20	2.30	2.38	е		2.286 BSC	
A1	0.00	-	0.20	н	9.40	10.10	10.50
A2	0.90	1.07	1.17	L	1.38	1.50	1.75
b	0.68	0.78	0.90	L1		2.90 REF	
b3	5.23	5.33	5.46	L2		0.51 BSC	
С	0.43	0.53	0.61	L3	0.88	-	1.28
D	5.98	6.10	6.22	L4	0.50		1.00
D1		5.30 REF		L5	1.65	1.80	1.95
E	6.40	6.60	6.73	θ	0°	-	8°
E1	4.63	-	-				

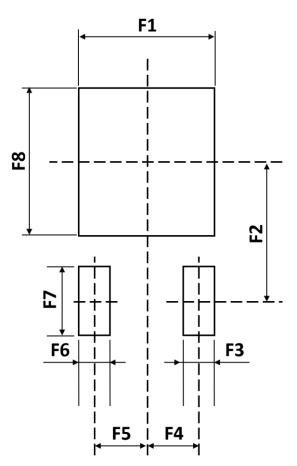
ORDERING INFORMATION

Part Number	Package	Packing	Reel Qty.	Inner Box Qty.	Outer Box Qty.
CEU6040SL	TO252 (DPAK)	Reel	2,500pcs	5,000pcs	40,000pcs

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RECOMMENDED PAD LAYOUT



Sym	Millimeters (Min.)	Millimeters (Typ.)	Millimeters (Max.)	Sym	Millimeters (Min.)	Millimeters (Typ.)	Millimeters (Max.)
F1	-	6.00	-	F5	-	2.29	-
F2	-	6.25	-	F6	-	1.40	-
F3	-	1.40	-	F7	-	3.00	-
F4	-	2.29	-	F8	-	6.50	-

Notes:

1. The suggested land pattern dimensions have been provided for reference only.

2. For further information, please reference document IPC-7351A.

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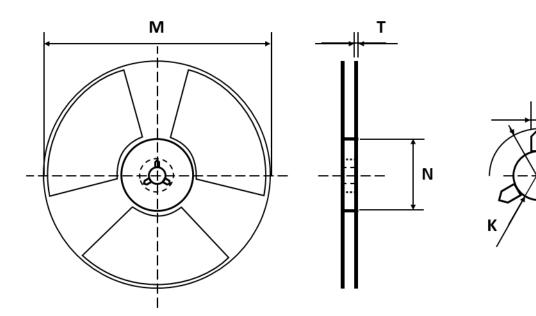


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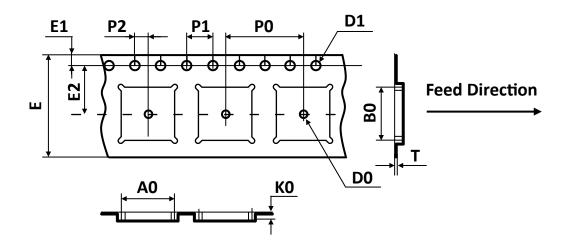


REEL DIMENSIONS All dimensions in mm



Tape Size	Reel Size	М	N	т	Н	К	S
	16mm Ø330	Ø330.00	Ø100.00	2.10	22.00	13.00	2.00
16mm		+2.00	+0 E0	±0.20	±0.50	+0.50	+0.50
		±2.00 ±0.50		±0.20	±0.50	-0.20	-0.20

TAPE DIMENSIONS All dimensions in mm



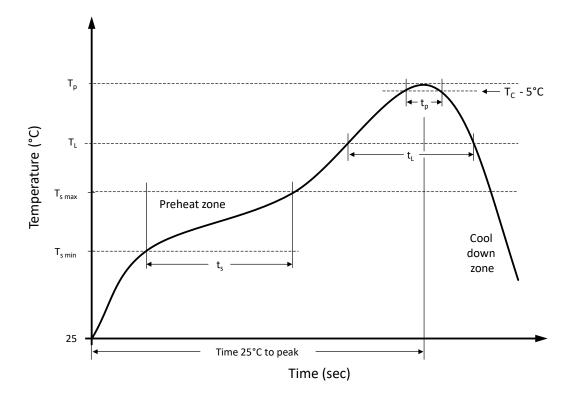
Package	A0	B0	К0	D0	D1	E	E1	E2	P0	P1	P2	Т
TO252	6.90	10.50	2.70	1.50	1.50	16.00	1.75	7.50	8.00	4.00	2.00	0.30
(DPAK)	±0.10	±0.10	±0.10	MIN	±0.10	+0.30 -0.20	±0.10	±0.10	±0.10	±0.10	±0.10	±0.05







RECOMMENDED REFLOW SOLDERING PROFILE



Recommended reflow soldering conditions ▲ **Refer to JEDEC J-STD-020E**

Profile Features		Sn-Pb Eutetic Assembly	Pb-Free Assembly
Preheat temperature min.	T_{smin}	100 °C	150 °C
Preheat temperature max.	$T_{s max}$	150 °C	200 °C
Preheat time t_s from $T_{s min}$ to $T_{s max}$	ts	120 seconds	120 seconds
Ramp-up rate (T _L to T _p)		max. 3 °C/second	max. 3 °C/second
Liquidous temperature	ΤL	183 °C	217 °C
Time t_L maintained above T_L	t∟	150 seconds max.	150 seconds max.
Peak package body temperature	Tp	235°C	260°C
Timeframe of within 5°C below and up to max actual peak body temperature	tp	20 seconds max.	30 seconds max.
Ramp-down rate (T_L to T_p)		max. 6 °C/second	max. 6 °C/second
Time 25°C to peak temperature		max. 6 minutes	max. 8 minutes

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REVISION TABLE

Revision	Date	Status	Notes
001	30/09/2022	Initial release	Initial publication

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