









CEW46N65S

650V ▲ 46mΩ ▲ 46.7A ▲ Si MOSFET

SILICON Si MOSFET ▲ THT type
N-channel enhancement mode
UL94V-0 rated flame retardant epoxy
TO247-3L package

Super high dense cell density for extremely low R_{DS(ON)} **High power and current handling capability**

Parameter (T _c = 25°C, unless otherwise noted)		Characteristics
Drain-Source Voltage	V _{DS}	650V
Gate-Source Voltage	V _{GS}	±20V
Continuous Drain Current at T _C = 25°C	I _D	46.7A
Continuous Drain Current at T _C = 100°C	I _D	29.5A
Pulsed Drain Current Note 1	I _{DM}	187A
Maximum Power Dissipation at T _C = 25°C	P _D	305W
Power Dissipation Derating above 25°C	ΔP_D	2.44W/°C
Single Pulsed Avalanche Energy Note 4	E _{AS}	300mJ
Single Pulsed Avalanche Current Note 4	l _{AS}	4A
Operating and Storage Temperature Range	T_J , T_{STG}	-55°C to +150°C

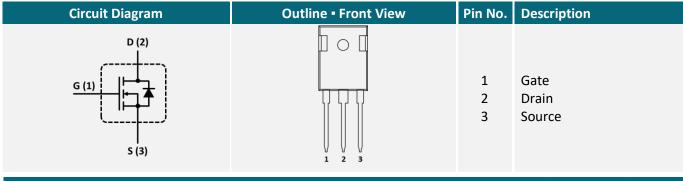
THERMAL CHARACTERISTICS

Parameter	Symbol	Limit
Thermal Resistance, Junction-to-Case	R _{TH_JC}	0.41°C/W
Thermal Resistance, Junction-to-Ambient	R _{TH JA}	62.5°C/W

APPLICATIONS

EV Charging	Industrial Inverters	Motors & Drives	Power Factor Correction	Renewable Energy	SMPS	UPS
₹			PFC	*		

PIN DESCRIPTION





ELECTRICAL CHARACTERISTICS ▲ T_C = 25°C, unless otherwise noted

Item	Condition	Symbol	Min.	Тур.	Max.	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	$V_{GS} = 0V$, $I_D = 250\mu A$	BV_DSS	650			V
Zero Gate Voltage Drain Current	$V_{DS} = 650V, V_{GS} = 0V$	I _{DSS}			1	μΑ
Gate Body Leakage Current, Forward	$V_{GS} = 20V, V_{DS} = 0V$	I_{GSSF}			100	nA
Gate Body Leakage Current, Reverse	$V_{GS} = -20V$, $V_{DS} = 0V$	I_{GSSR}			-100	nA
On Characteristics Note 2						
Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 250 \mu A$	$V_{GS(th)}$	2.5		4.5	V
Static Drain-Source On-Resistance	$V_{GS} = 10V$, $I_D = 20A$	R _{DS(ON)}		46	56	mΩ
Dynamic Characteristics Note 3						
Input Capacitance	$V_{DS} = 100V$, $V_{GS} = 0V$, $f = 1MHz$	C _{ISS}		2935		pF
Output Capacitance	$V_{DS} = 100V$, $V_{GS} = 0V$, $f = 1MHz$	Coss		125		pF
Reverse Transfer Capacitance	$V_{DS} = 100V$, $V_{GS} = 0V$, $f = 1MHz$	C_{RSS}		10		pF
Switching Characteristics Note 3						
Turn-On Delay Time	V_{DD} = 520V, V_{GS} = 10V, I_D = 10A, $R_{G(ext)}$ = 10 Ω	t _{D(ON)}		45		ns
Turn-On Rise Time	V_{DD} = 520V, V_{GS} = 10V, I_D = 10A, $R_{G(ext)}$ = 10 Ω	t _R		23		ns
Turn-Off Delay Time	V_{DD} = 520V, V_{GS} = 10V, I_D = 10A, $R_{G(ext)}$ = 10 Ω	t _{D(OFF)}		199		ns
Turn-Off Fall Time	V_{DD} = 520V, V_{GS} = 10V, I_D = 10A, $R_{G(ext)}$ = 10 Ω	t _F		10		ns
Total Gate Charge	$V_{DS} = 520V$, $V_{GS} = 10V$, $I_D = 10A$	Q_{G}		100		nC
Gate Source Charge	$V_{DS} = 520V$, $V_{GS} = 10V$, $I_D = 10A$	Q_{GS}		17		nC
Gate Drain Charge	$V_{DS} = 520V$, $V_{GS} = 10V$, $I_D = 10A$	Q_{GD}		41		nC
Drain-Source Diode Characteristics a	nd Maximum Ratings					
Drain-Source Diode Forward Current		Is			46.7	Α
Drain-Source Diode Forward Voltage Note 2	V _{GS} = 0V, I _S = 20A	V_{SD}			1.5	V
Reverse Recovery Time	$I_D = 20A$, di/dt = 75A/ μ s	t_{RR}		449		ns
Reverse Recovery Charge	$I_D = 20A$, di/dt = 75A/ μ s	Q_{RR}		5.71		μC
Peak Reverse Recovery Current	$I_D = 20A$, di/dt = 75A/ μ s	I_{RR}		21.7		Α

Notes

- 1: Repetitive Rating: Pulse width limited by maximum junction temperature
- 2: Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 3: Guaranteed by design, not subject to production testing.
- 4: L = 37.5mH, $I_{AS} = 4$ A, $V_{DD} = 60$ V, $R_G = 25$ Ω, Starting $T_J = 25$ °C



REFERENCE DATA A TYPICAL DEVICE PERFORMANCE



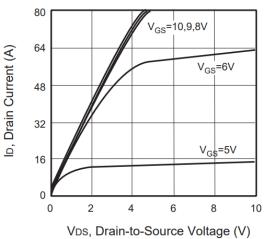


Fig. 2 • Transfer Characteristics

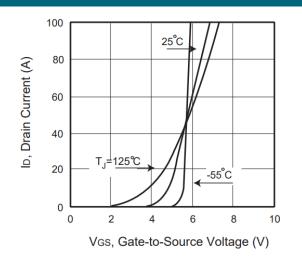


Fig. 3 • Capacitance

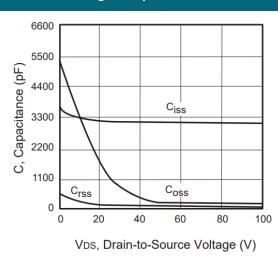


Fig. 4 • On-Resistance Variation with Temperature

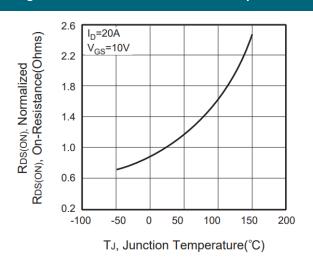


Fig. 5 • Gate Threshold Variation with Temperature

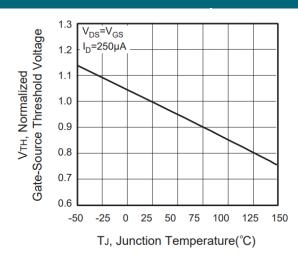
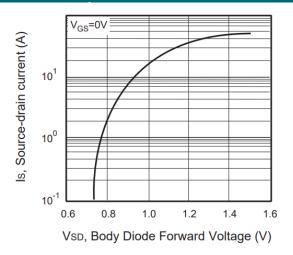


Fig. 6 • Body Diode Forward Voltage Variation with Source Current



MGT ▲ Manufacturer Group of Technology



REFERENCE DATA A TYPICAL DEVICE PERFORMANCE

Fig. 7 • Gate Charge

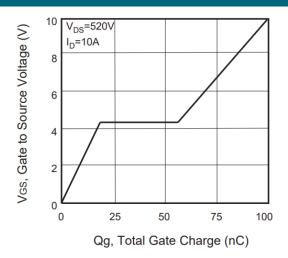


Fig. 8 • Maximum Safe Operating Area

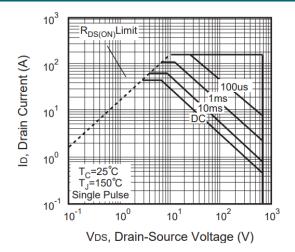
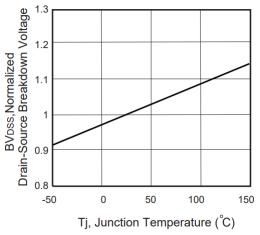


Fig. 9 • Breakdown Voltage Variation vs. Temperature





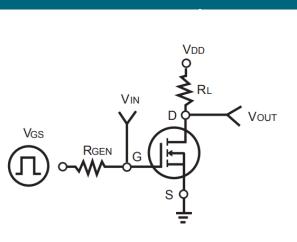
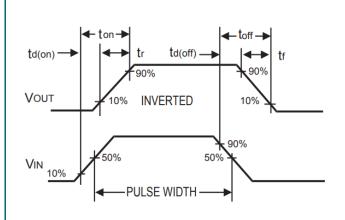


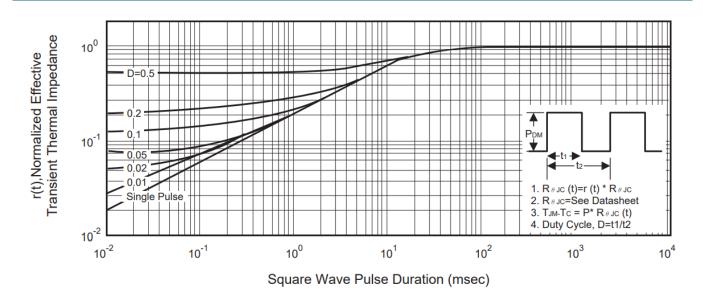
Fig. 11 • Switching Waveforms





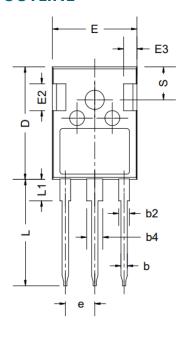
REFERENCE DATA A TYPICAL DEVICE PERFORMANCE

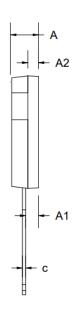
Fig. 12 • Normalized Thermal Transient Impedance Curve

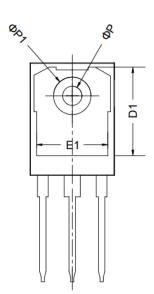




PACKAGE OUTLINE









Sym	Millimeters (Min.)	Millimeters (Typ.)	Millimeters (Max.)
Α	4.80	5.00	5.20
A1	2.21	2.41	2.59
A2	1.85	2.00	2.15
b	1.11	1.21	1.36
b2	1.91	2.01	2.21
b4	2.91	3.01	3.21
С	0.51	0.61	0.75
D	20.80	21.00	21.30
D1	16.25	16.55	16.85
Е	15.50	15.80	16.10

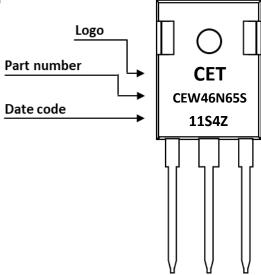
Sym	Millimeters (Min.)	Millimeters (Typ.)	Millimeters (Max.)		
E1	13.00	13.30	13.60		
E.T.	15.00	15.50	15.00		
E2	4.80	5.00	5.20		
E3	2.30	2.50	2.70		
e	5.44 BSC				
L	19.62	19.92	20.22		
L1	-	-	4.30		
ØΡ	3.40	3.60	3.80		
ØP1	-	-	7.30		
S	6.16 BSC				

ORDERING INFORMATION

Part Number	Package	Packing	Tube Qty.	Inner Box Qty.	Outer Box Qty.
CEW46N65S	TO-247-3L	Tube	30pcs	450pcs	1,800pcs



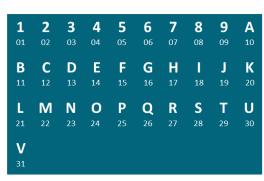
PART MARKING



DATE CODE

Example: 11S4Z



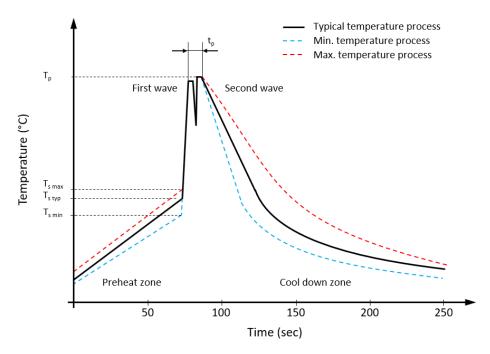


Coding list for "Day"





RECOMMENDED WAVE SOLDERING PROFILE A THT PACKAGE



Classification wave soldering profile ▲ Refer to EN 61760-1: 2006

Profile Features		Value ▲ Sn-Pb Assembly	Value ▲ Pb-free Assembly
Preheat temperature min.	T_{smin}	100 °C	100 °C
Preheat temperature typical	T _{s typ}	120 °C	120 °C
Preheat temperature max.	T_{smax}	130 °C	130 °C
Preheat time t_s from T_{smin} to T_{smax}	t_s	70 seconds	70 seconds
Peak temperature	T_p	235 °C to 260 °C	245 °C to 260 °C
Time of actual peak temperature	t _p	Max. 10 seconds Max. 5 second each wave	Max. 10 seconds Max. 5 second each wave
Ramp-down date min.		~ 2 °C/second	~ 2 °C/second
Ramp-down rate typical		~ 3.5 °C/second	~ 3.5 °C/second
Ramp-down rate max.		~ 5 °C/second	~ 5 °C/second
Time 25°C to 25°C		4 minutes	4 minutes



REVISION TABLE

Revision	Date	Status	Notes
001	30/09/2022	Initial release	Initial publication

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